

Figure 1

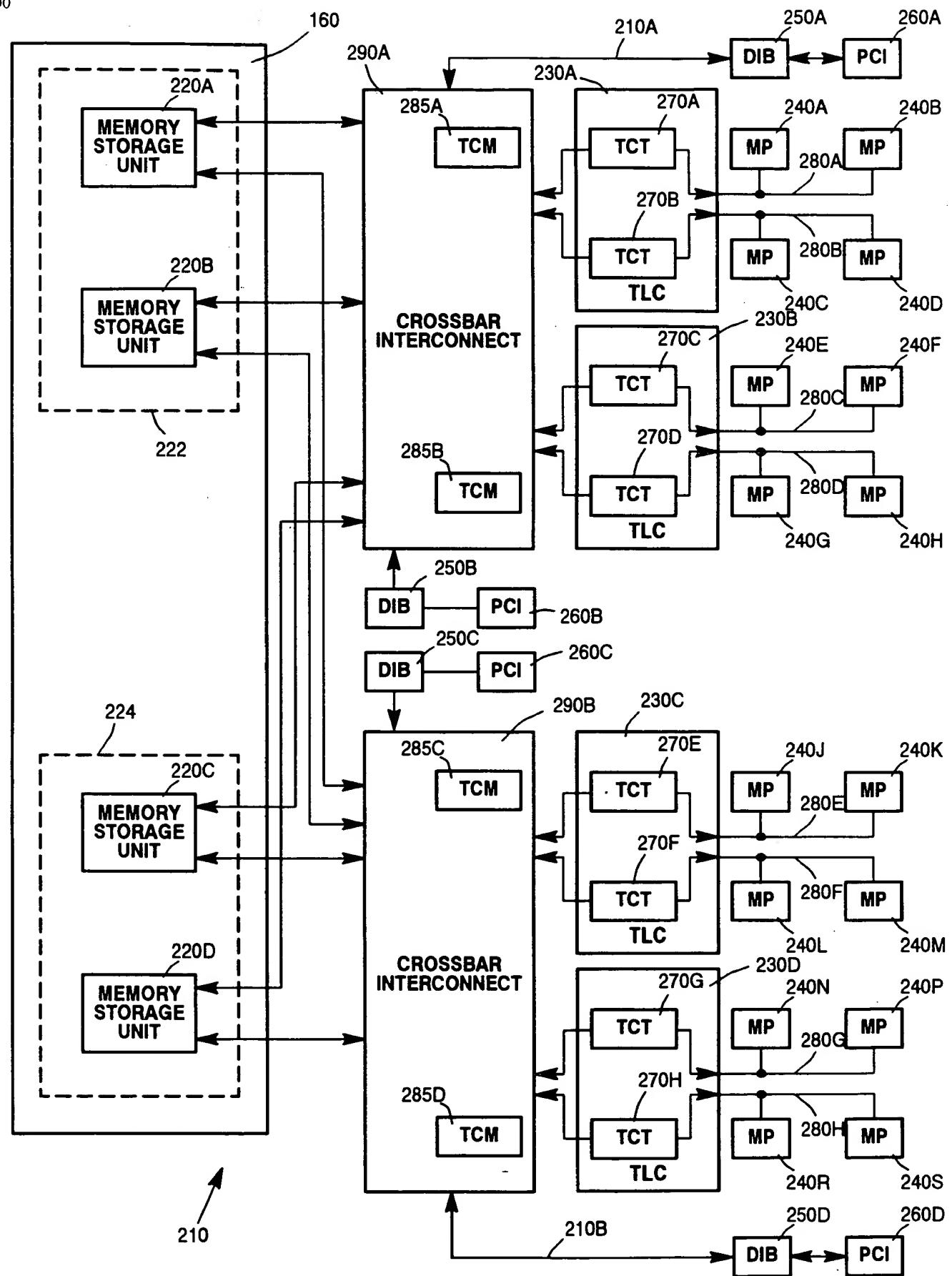


Figure 2

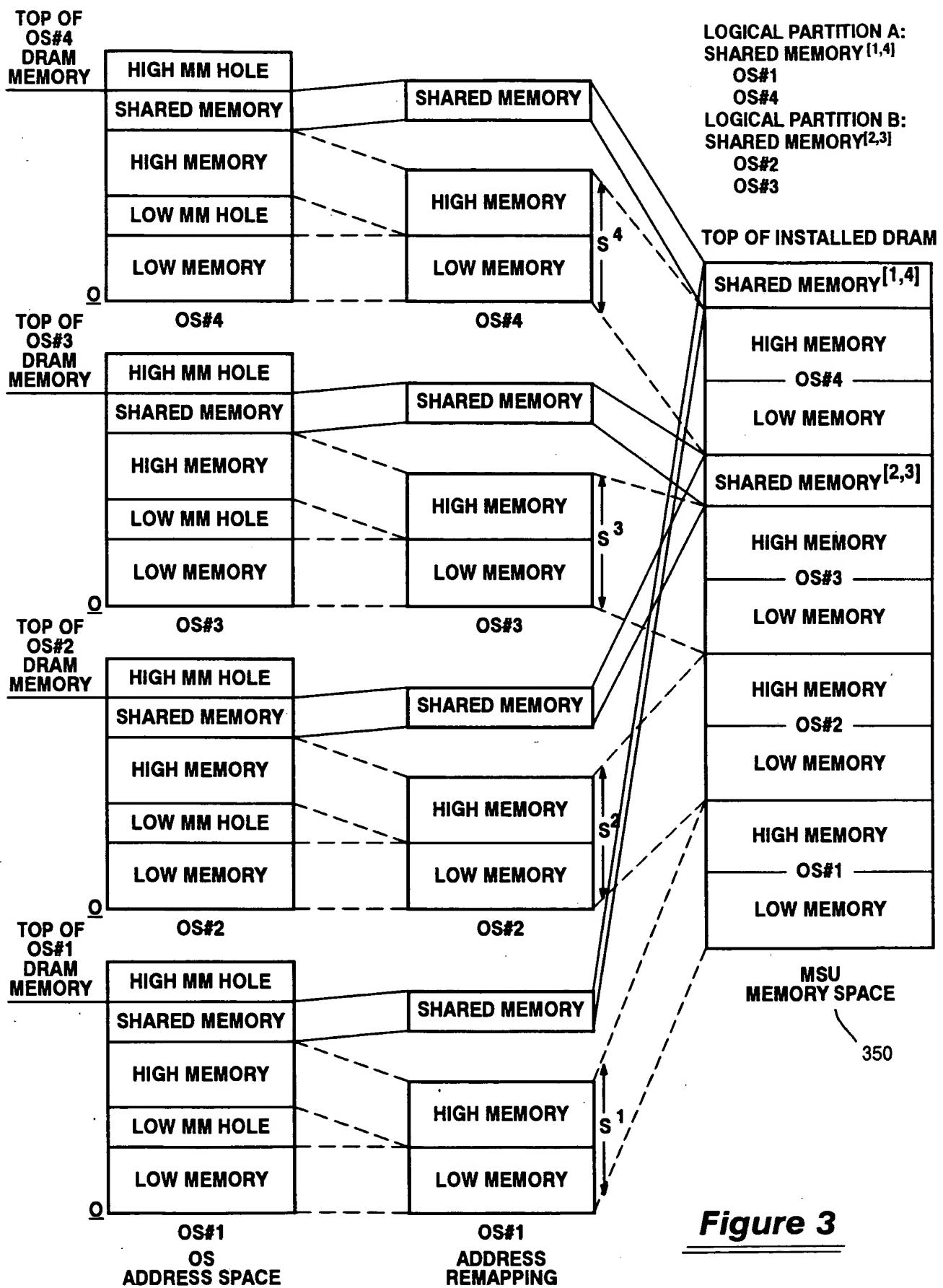


Figure 3

PCI/APIC HOLE BIOS CODE PCI/APIC HOLE BIOS CODE

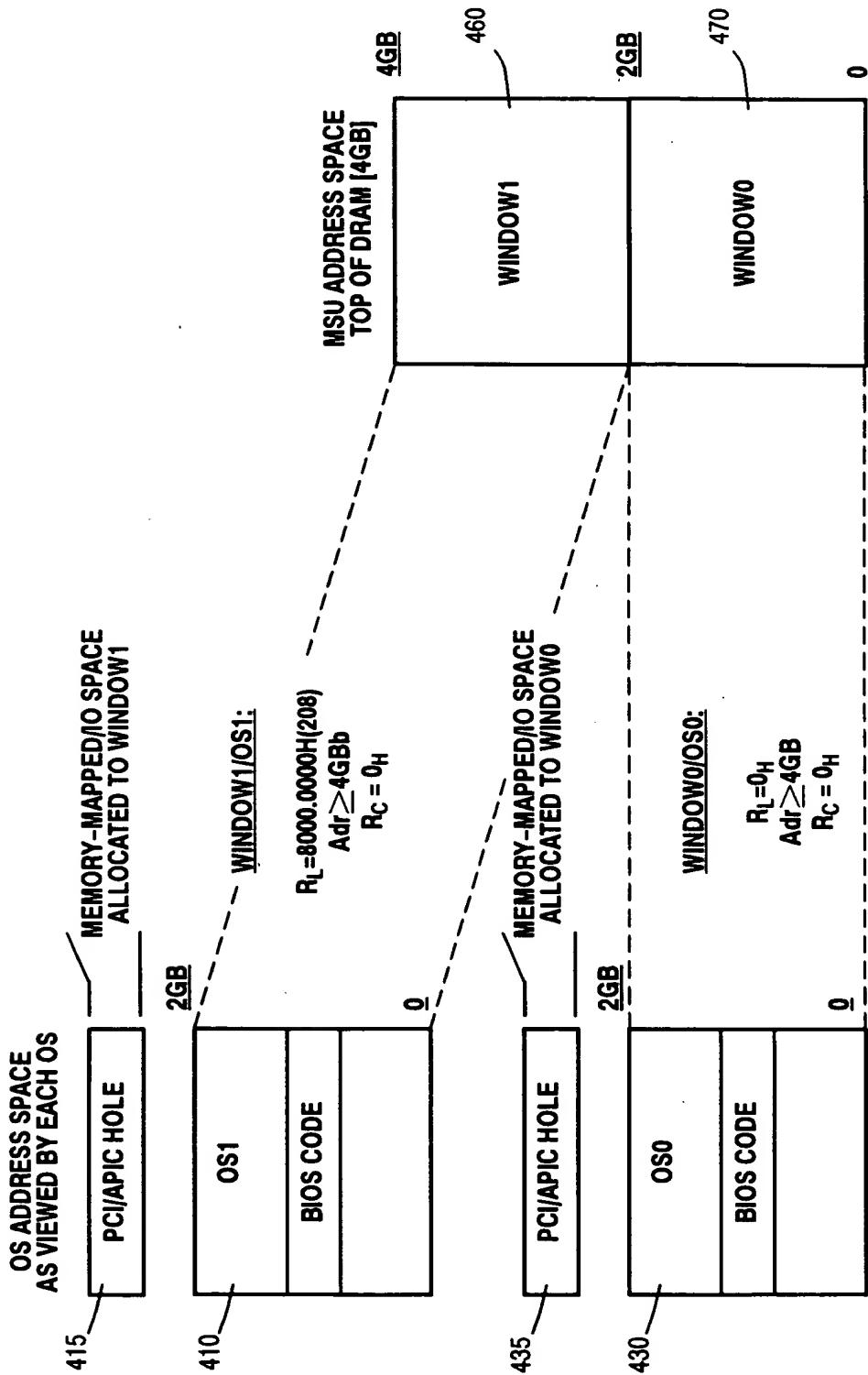


Figure 4

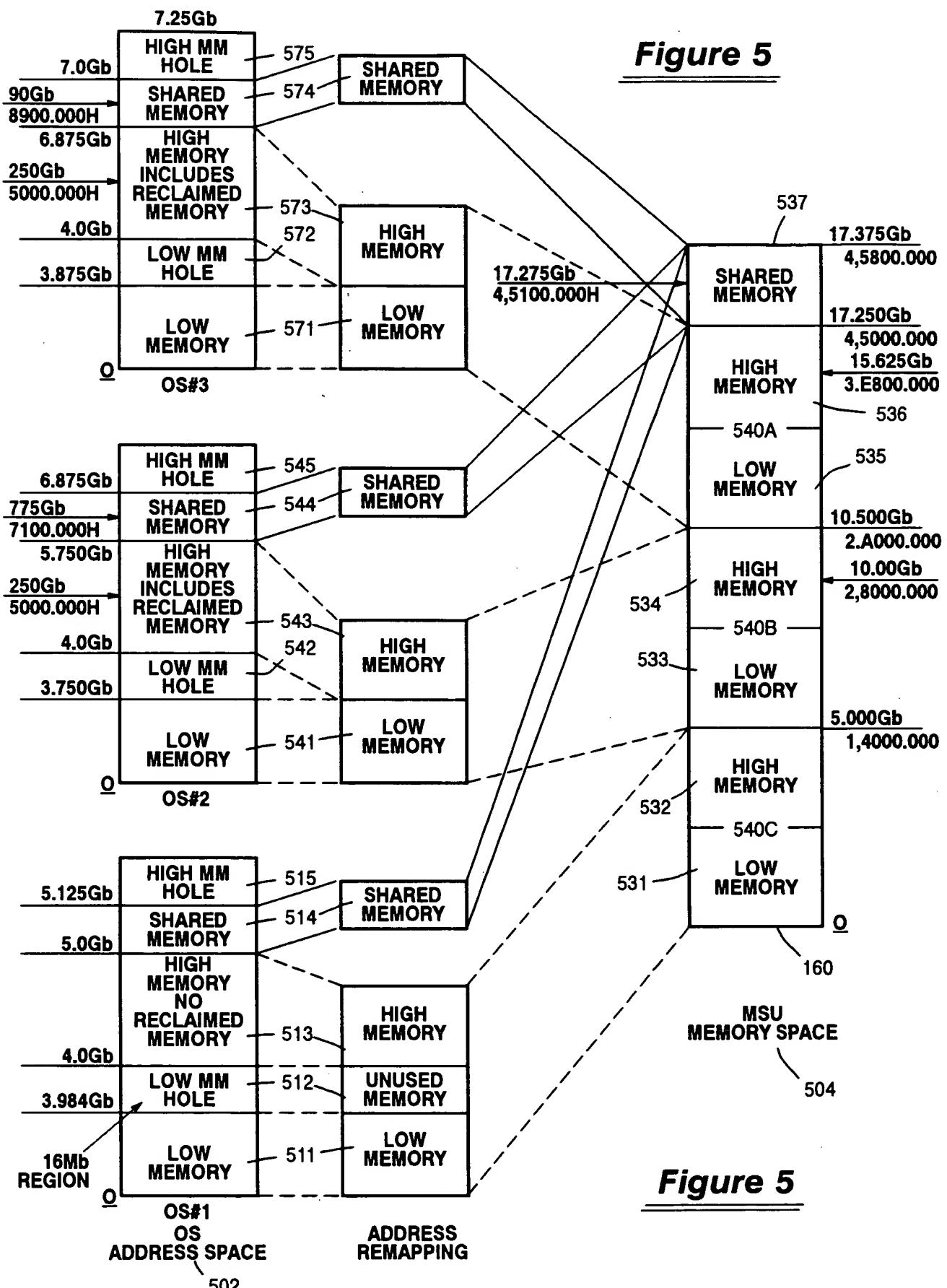


Figure 5

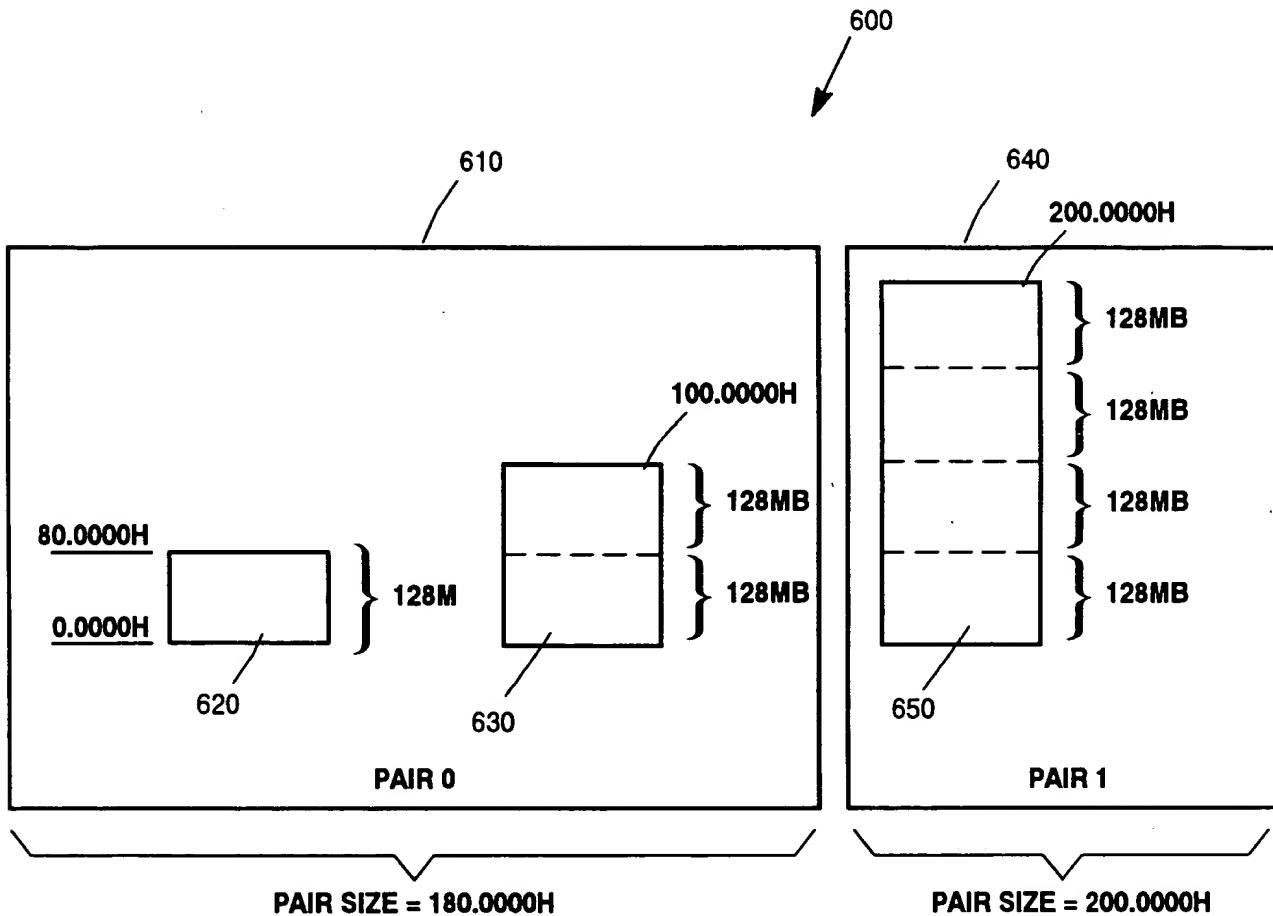


Figure 6

MSU #0 MSU #1 MSU #2 MSU #3

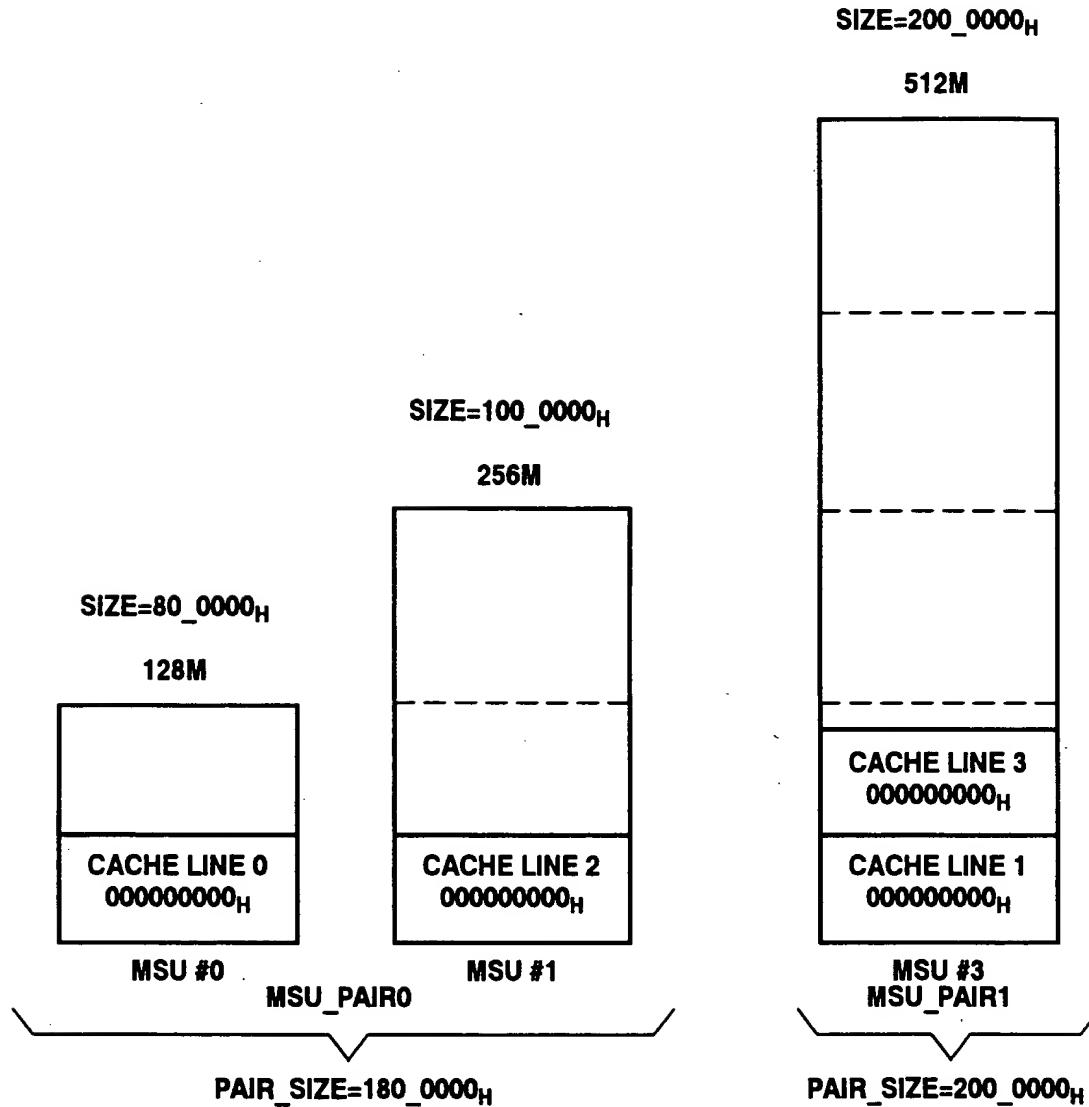


Figure 7

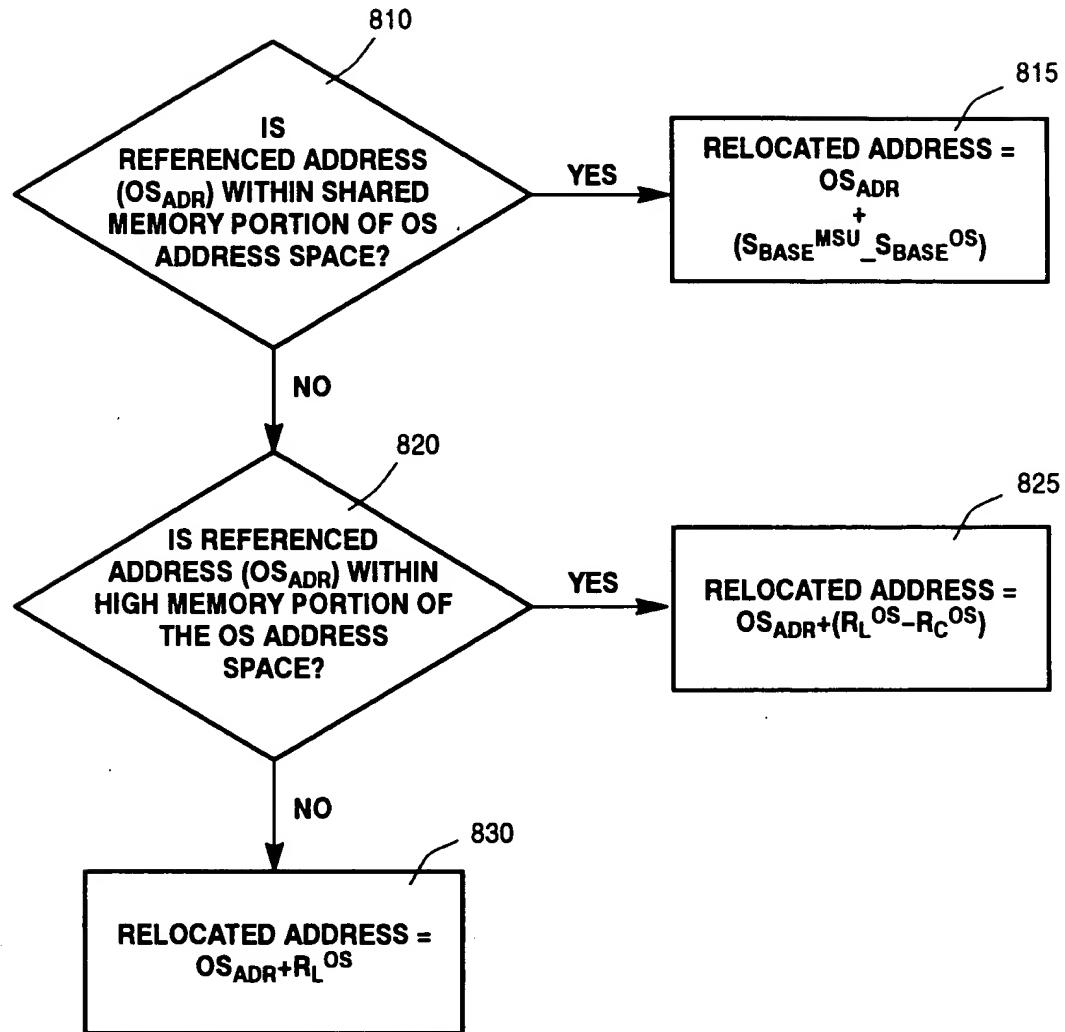


Figure 8

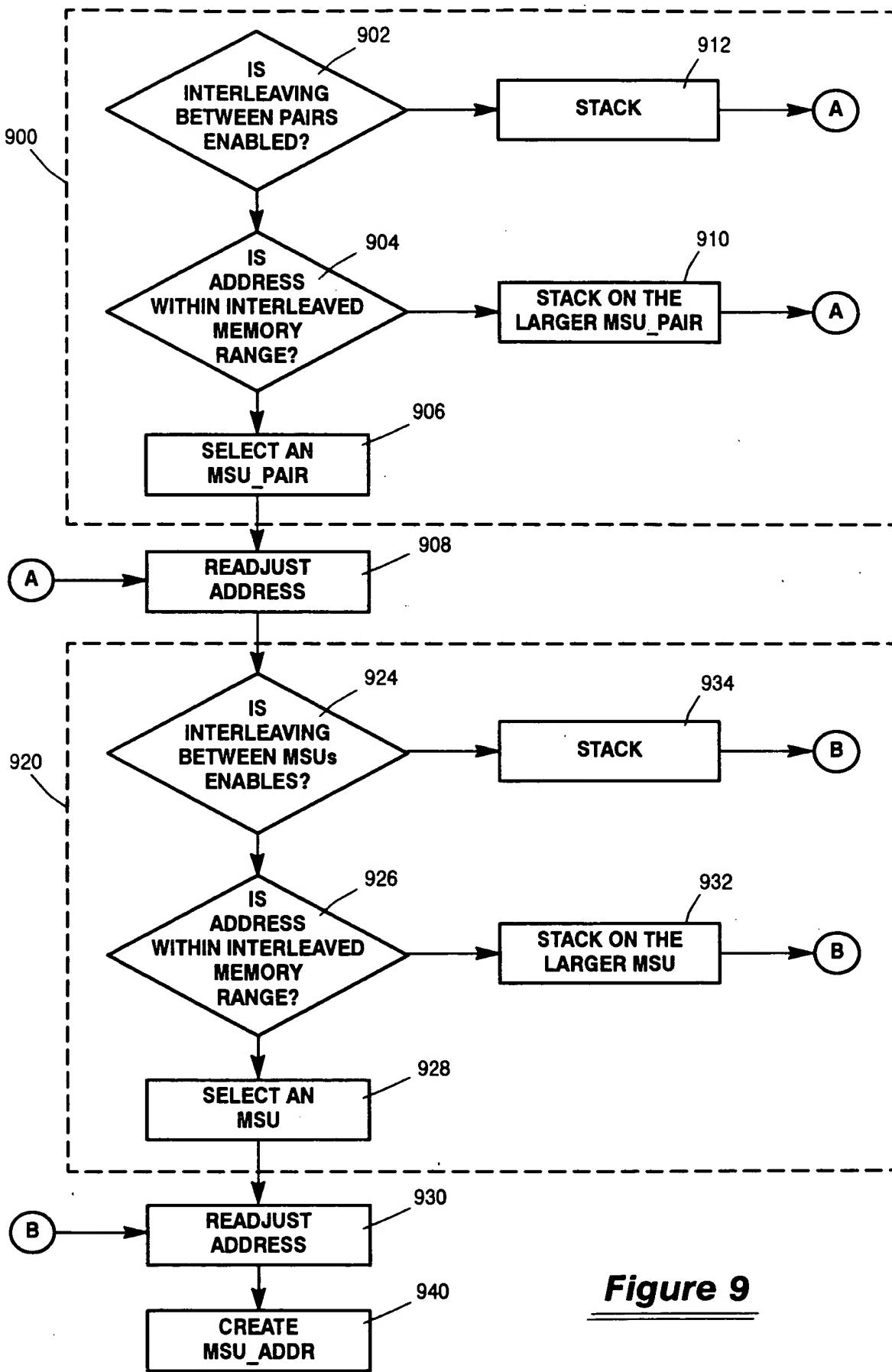


Figure 9

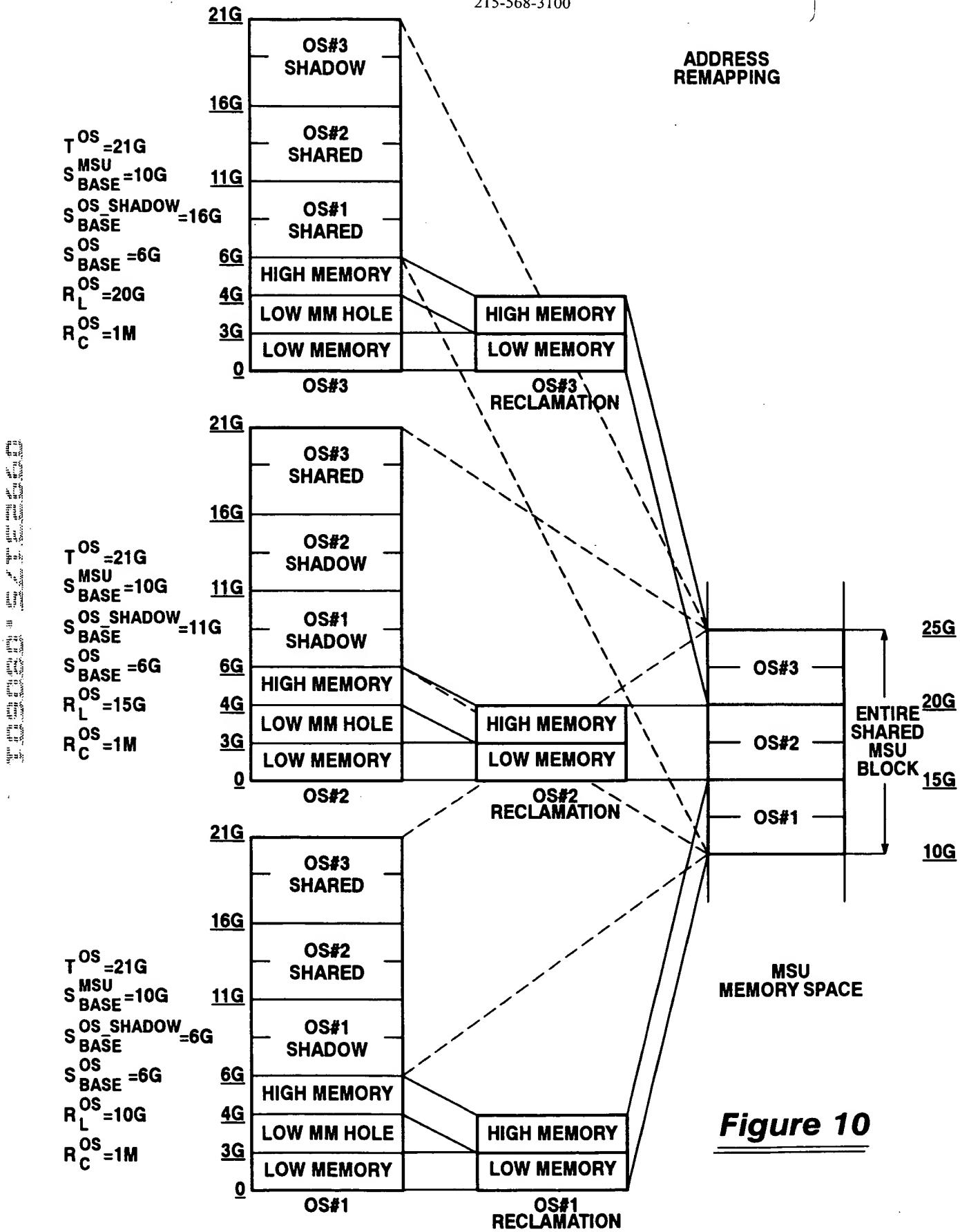


Figure 10

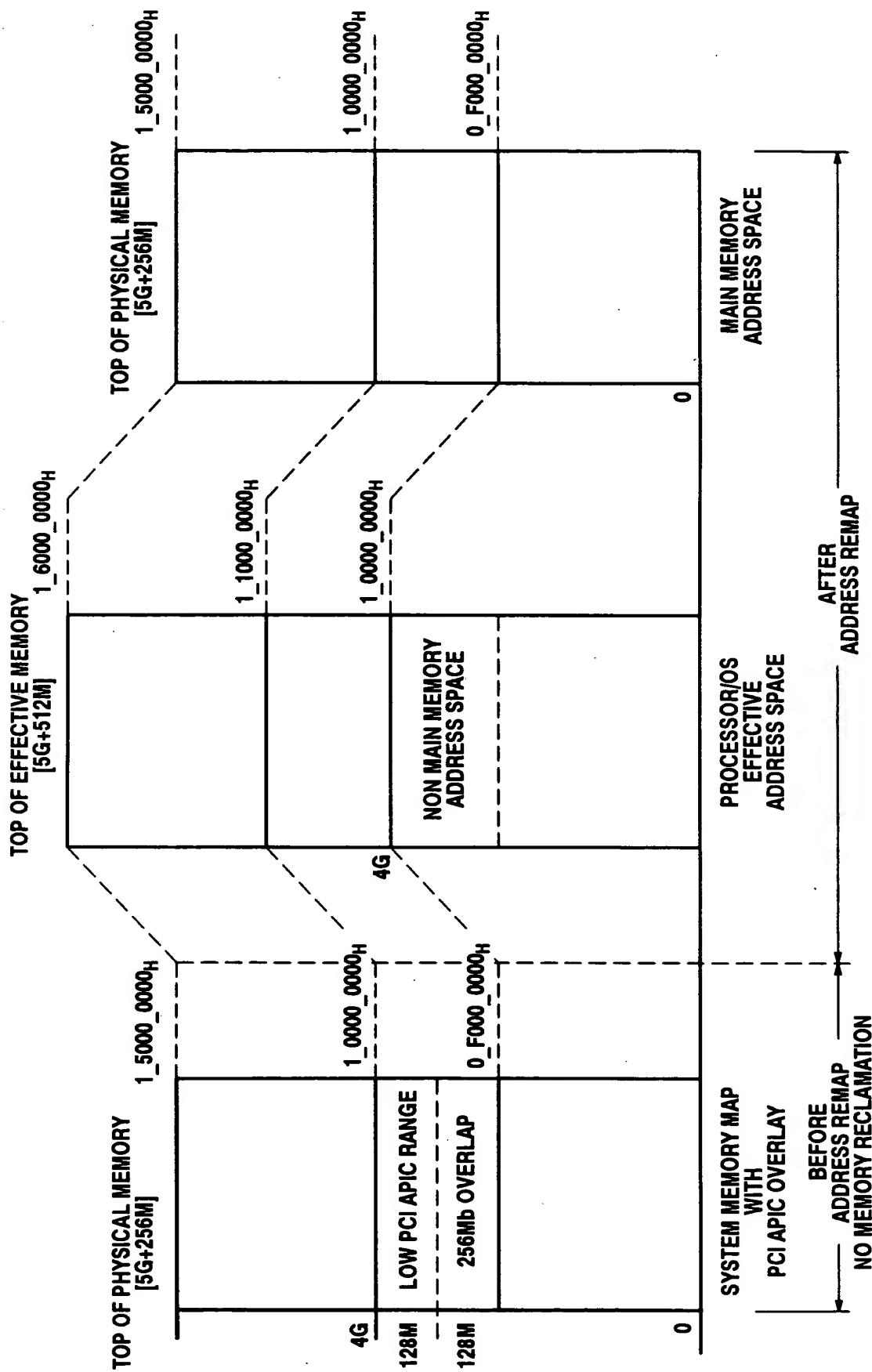


Figure 11

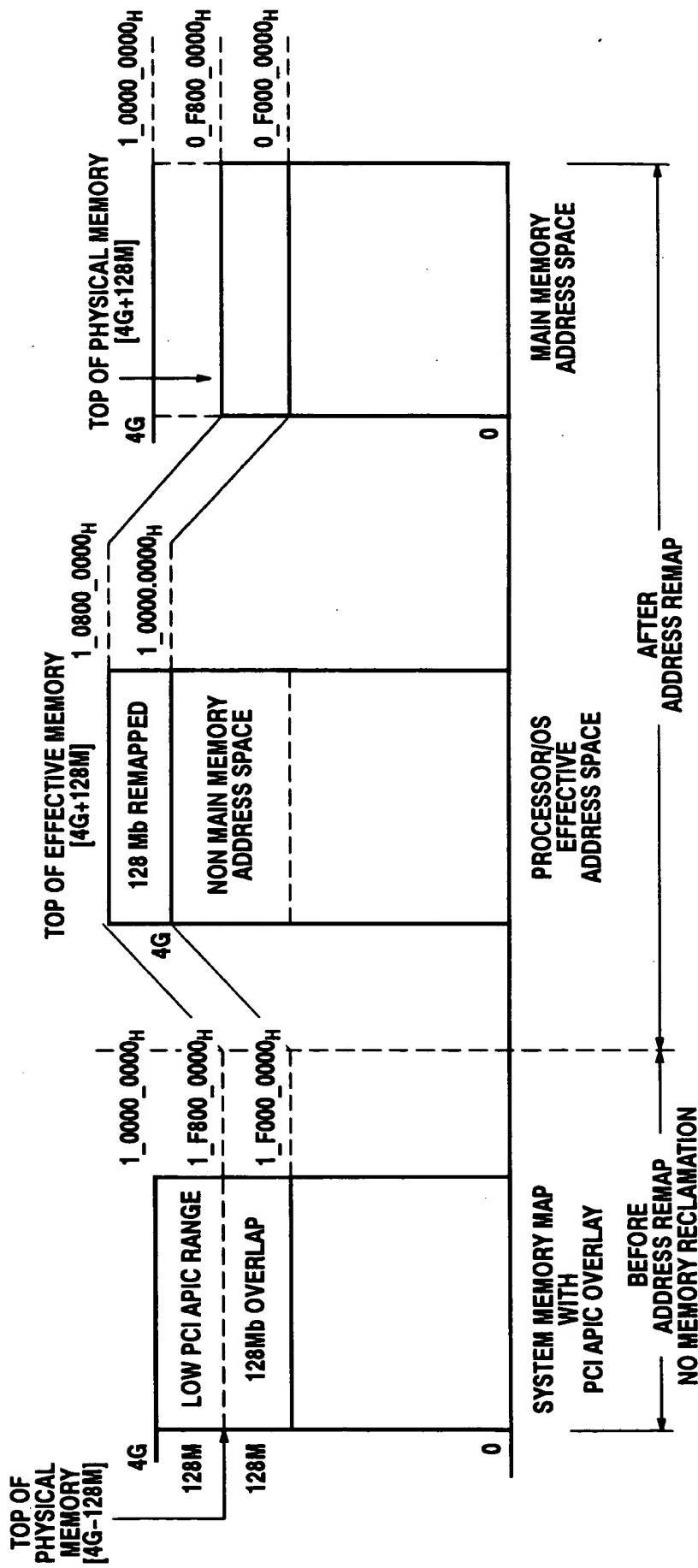


Figure 12

1000 900 800 700 600 500 400 300 200 100 0

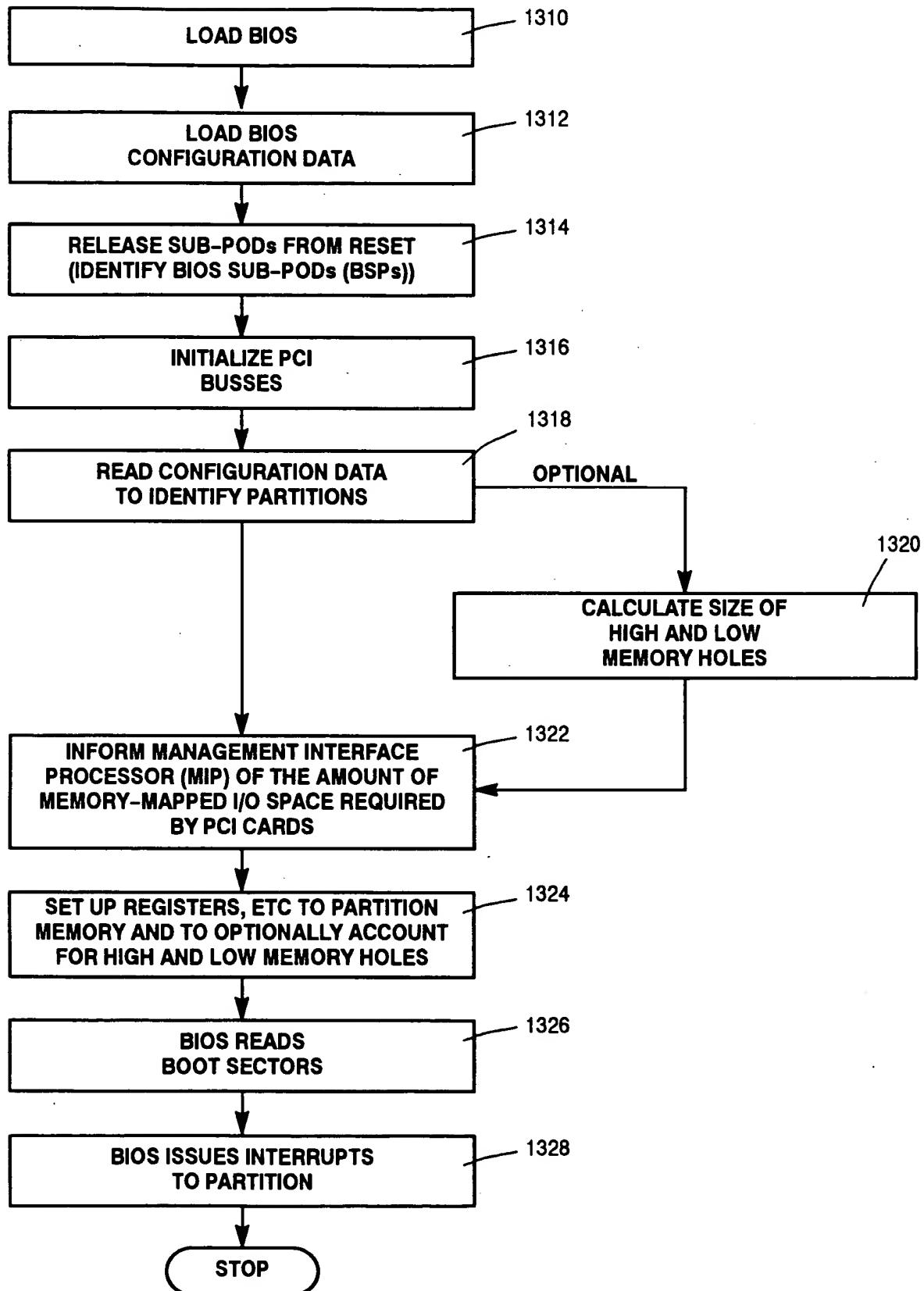


Figure 13

USYS-0094 "System and Method for Emulating Network Communications between Partitions of a Computer System"
S.B.Samuels-WoodcockWashburn et al.
215-568-3100

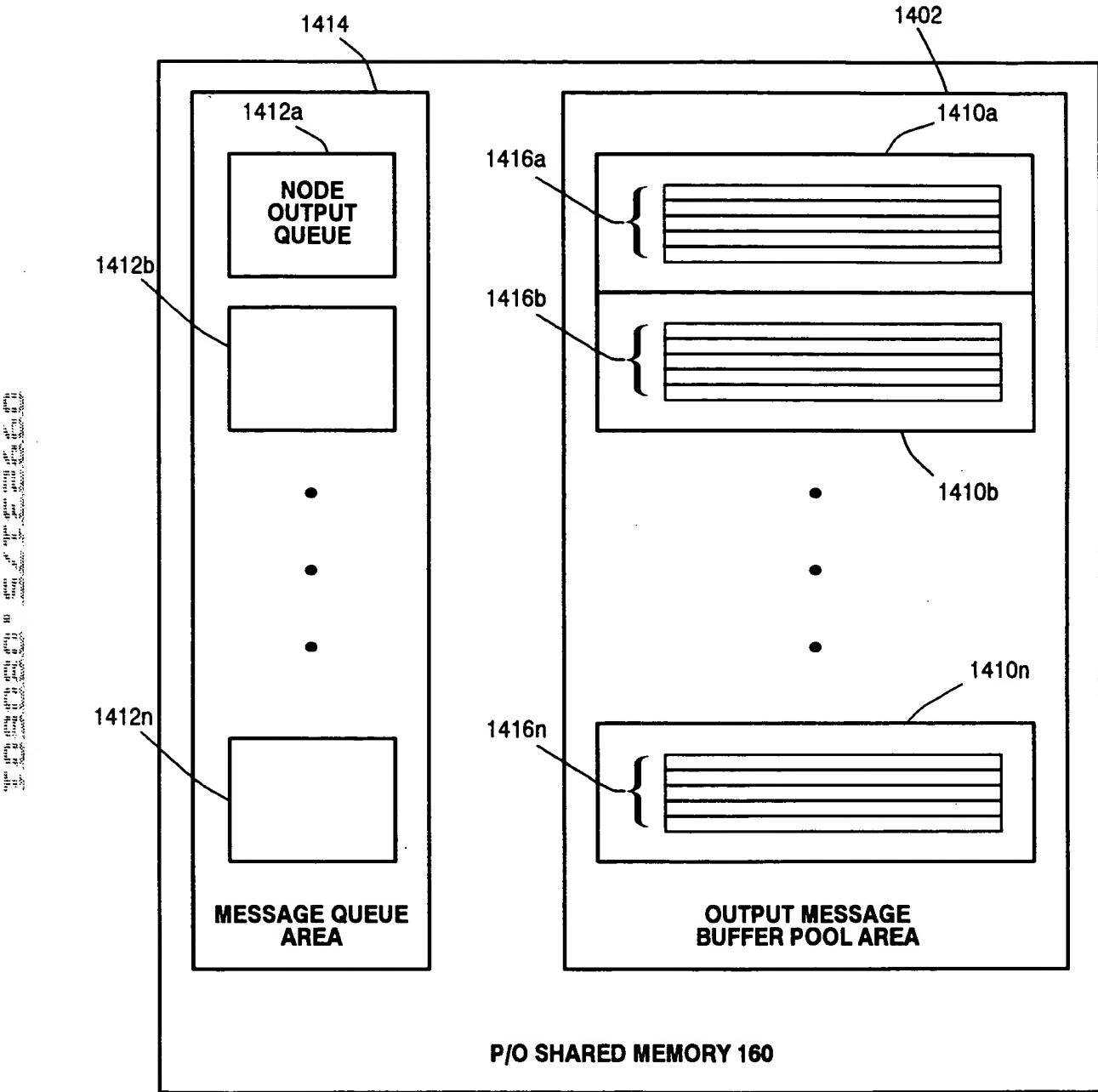


Figure 14

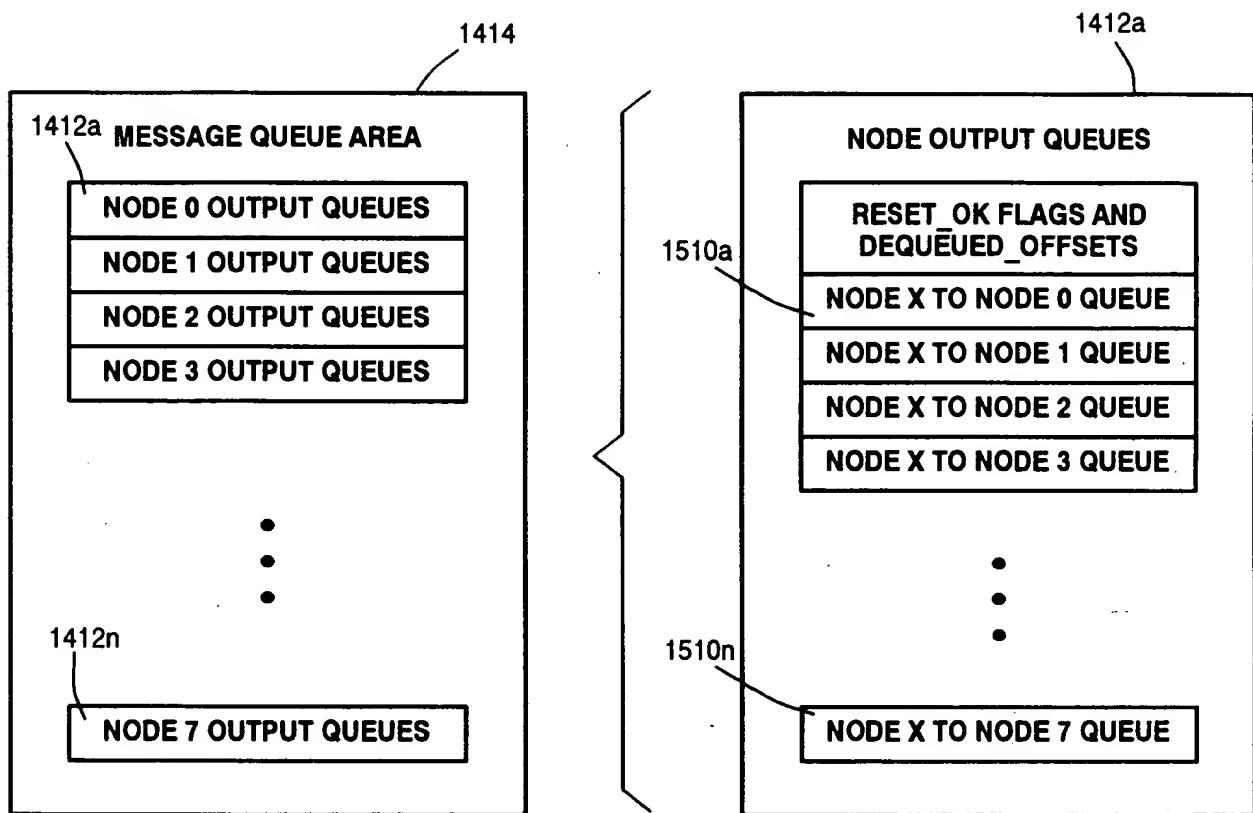


Figure 15

1412

A MORE DETAILED LOOK AT THE DEQUEUED_OFFSETS
AND THE MESSAGE QUEUES IS SHOWN BELOW:

	0	31	32 63				
0	RESERVED	NODE OS ID (EXAMPLES FOLLOW)					1610
		2 X	2 N	S C I T	O M U N		1612
1-2	RESERVED	NODE MAC ADDRESS (12 HEX DIGITS WITH 2 DIGITS PER BYTE)					
3-7	RESERVED	RESERVED					
0	RESERVED	32 RESET_OK	39 RESERVED	40 RESERVED	47 RESERVED	48 63 DEQUEUED_OFFSET FOR NODE 0	
1	RESERVED	RESET_OK	RESERVED	DEQUEUED_OFFSET FOR NODE 0			
2	RESERVED	RESET_OK	RESERVED	DEQUEUED_OFFSET FOR NODE 0			
				• • •			
7	RESERVED	RESET_OK	RESERVED	DEQUEUED_OFFSET FOR NODE 0			
START OF OUTPUT QUEUE TO NODE 0							
0	RESERVED	NEED_RESET	RESERVED	ENQUEUED_OFFSET FOR NODE 0			P/O NODE-NODE QUEUE 1510a

Figure 16A

1	RESERVED	MESSAGE BUFFER OFFSET		
2	RESERVED	MESSAGE BUFFER OFFSET		
		• • •		
511	RESERVED	MESSAGE BUFFER OFFSET		
START OF OUTPUT QUEUE TO NODE 1				
0	RESERVED	NEED_RESET	RESERVED	ENQUEUED_OFFSET FOR NODE 1
1	RESERVED	MESSAGE BUFFER OFFSET		
2	RESERVED	MESSAGE BUFFER OFFSET		
		• • •		
511	RESERVED	MESSAGE BUFFER OFFSET		
		• • •		
START OF OUTPUT QUEUE TO NODE 7				
0	RESERVED	NEED_RESET	RESERVED	ENQUEUED_OFFSET FOR NODE 7
1	RESERVED	MESSAGE BUFFER OFFSET		
2	RESERVED	MESSAGE BUFFER OFFSET		
		• • •		
511	RESERVED	MESSAGE BUFFER OFFSET		

P/O
NODE-
TO-NODE
QUEUE
1510a

1510

1510n

NODE_OS_ID IS A 4 CHARACTER STRING WITH ONE OF THE FOLLOWING VALUES:

- 'OS22' - OS2200 ARCHITECTURE
- 'MCP' - A-SERIES ARCHITECTURE
- 'UNIX' - INTEL ARCHITECTURE WITH A UNIX OPERATING SYSTEM
- 'NT' - INTEL ARCHITECTURE WITH MICROSOFT WINDOWS NT OPERATING SYSTEM

Figure 16B

1416

0	0	RESERVED	31	32	WORD LENGTH OF BUFFER		63	{ 1710 }	
1		RESERVED		WORD LENGTH OF HEADER					
2		RESERVED		32	47	48	63		
3		RESERVED		BYTE SKIP COUNT		BYTE TRANSFER COUNT			
n		RESERVED		BYTE SKIP COUNT		BYTE TRANSFER COUNT			
m		RESERVED		MESSAGE					
		RESERVED							
		RESERVED							
		RESERVED							
		RESERVED							
b-1		RESERVED		MESSAGE					

Figure 17

1802
1804

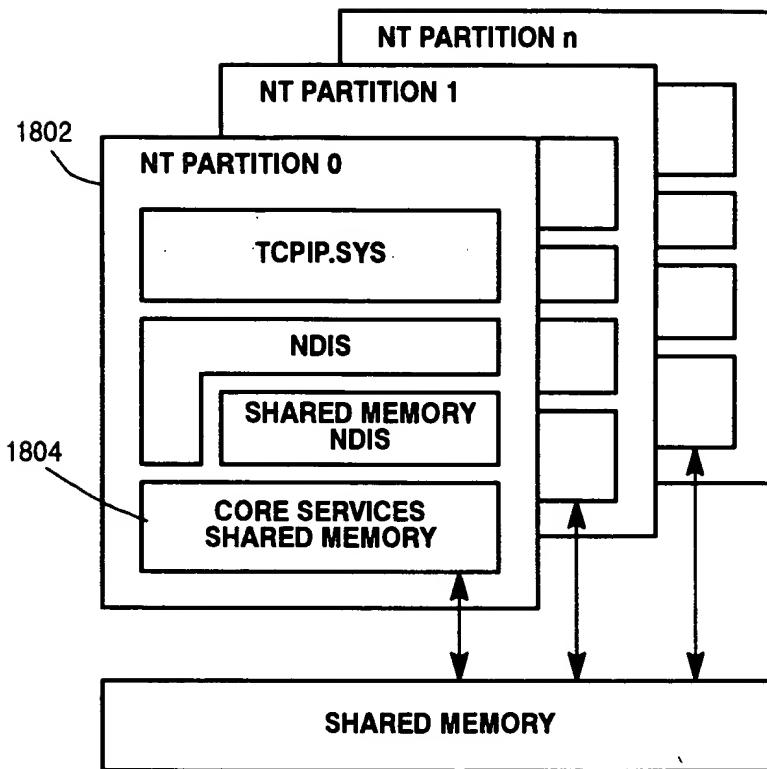


Figure 18

USYS-0094 "System and Method for Emulating Network Communications between Partitions of a Computer System"
S.B.Samuels-WoodcockWashburn et al.
215-568-3100

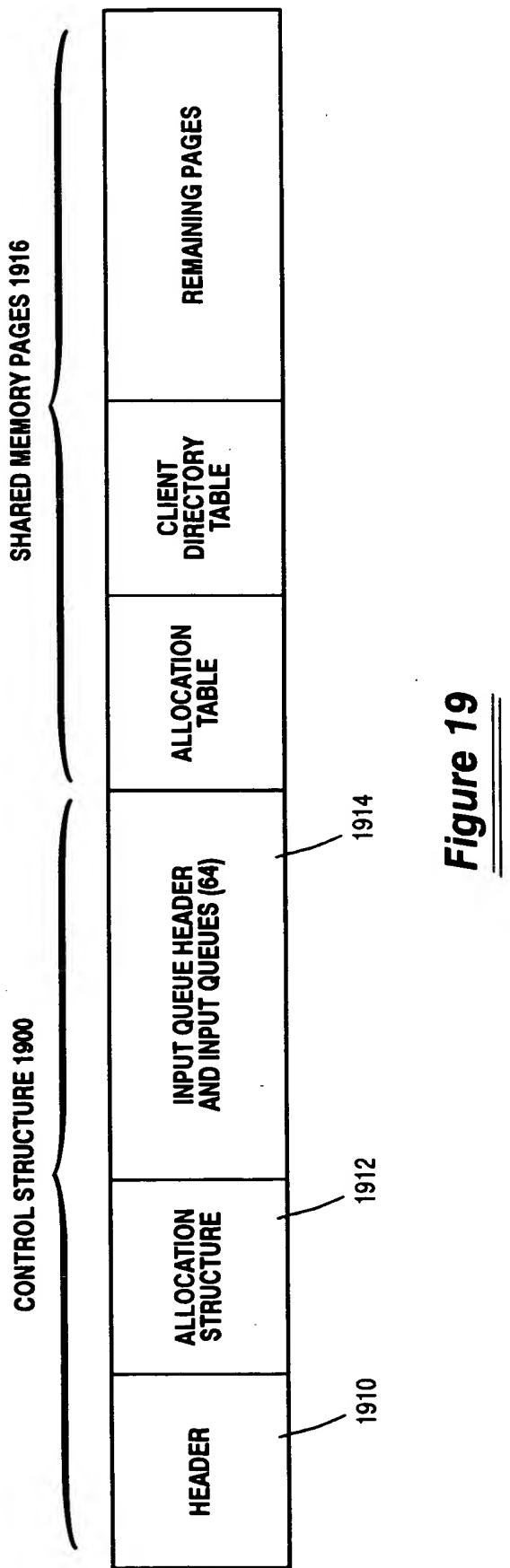


Figure 19

CONTROL STRUCTURE HEADER CONTENTS
VERSION ID
SHARED MEMORY STATUS
PARTITION ID OF "MASTER PARTITION"
SHARED MEMORY PARTITION CHECK IN INTERVAL
CLIENT DIRECTORY TABLE HEADER
PARTITION INFORMATION (10 WORDS PER PARTITION)

Figure 20

ALLOCATION STRUCTURE CONTENTS
ALLOCATION LOCK
LENGTH OF SHARED MEMORY AREA (IN 4K BYTES PAGES)
SHARED MEMORY PAGE POINTER
FREE PAGE LIST HEAD
ALLOCATION TABLE HEADER

Figure 21

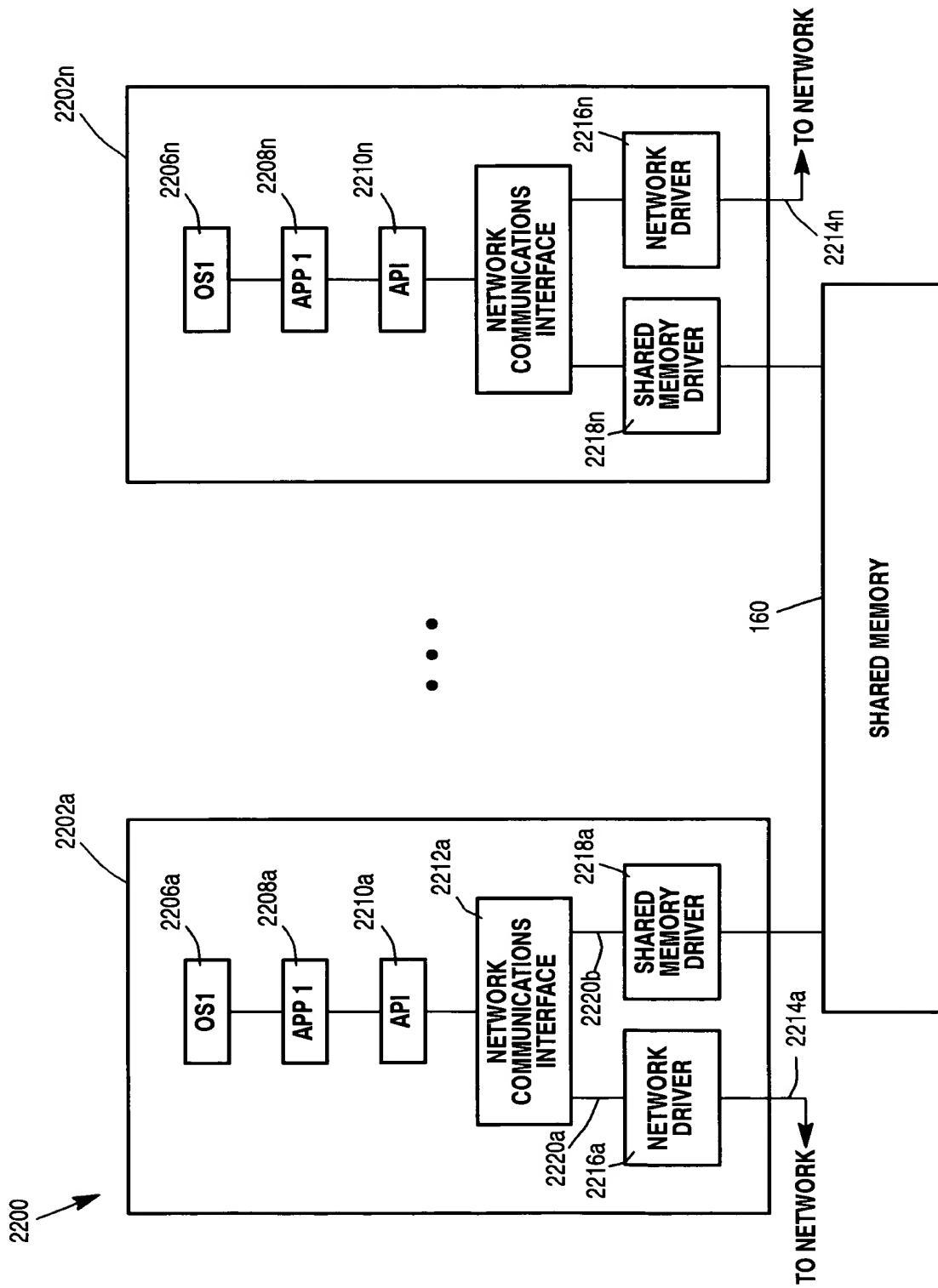


Figure 22

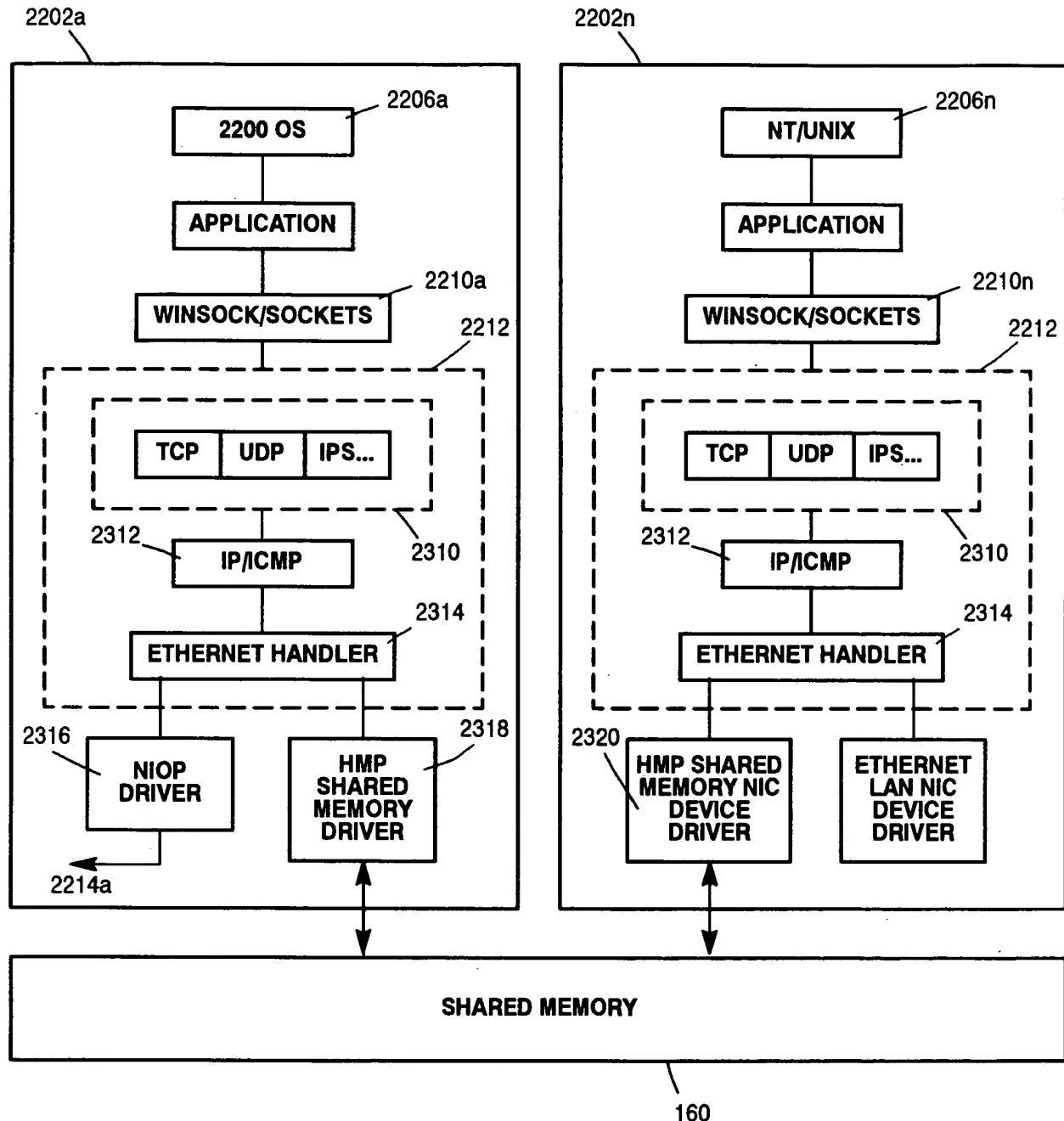


Figure 23

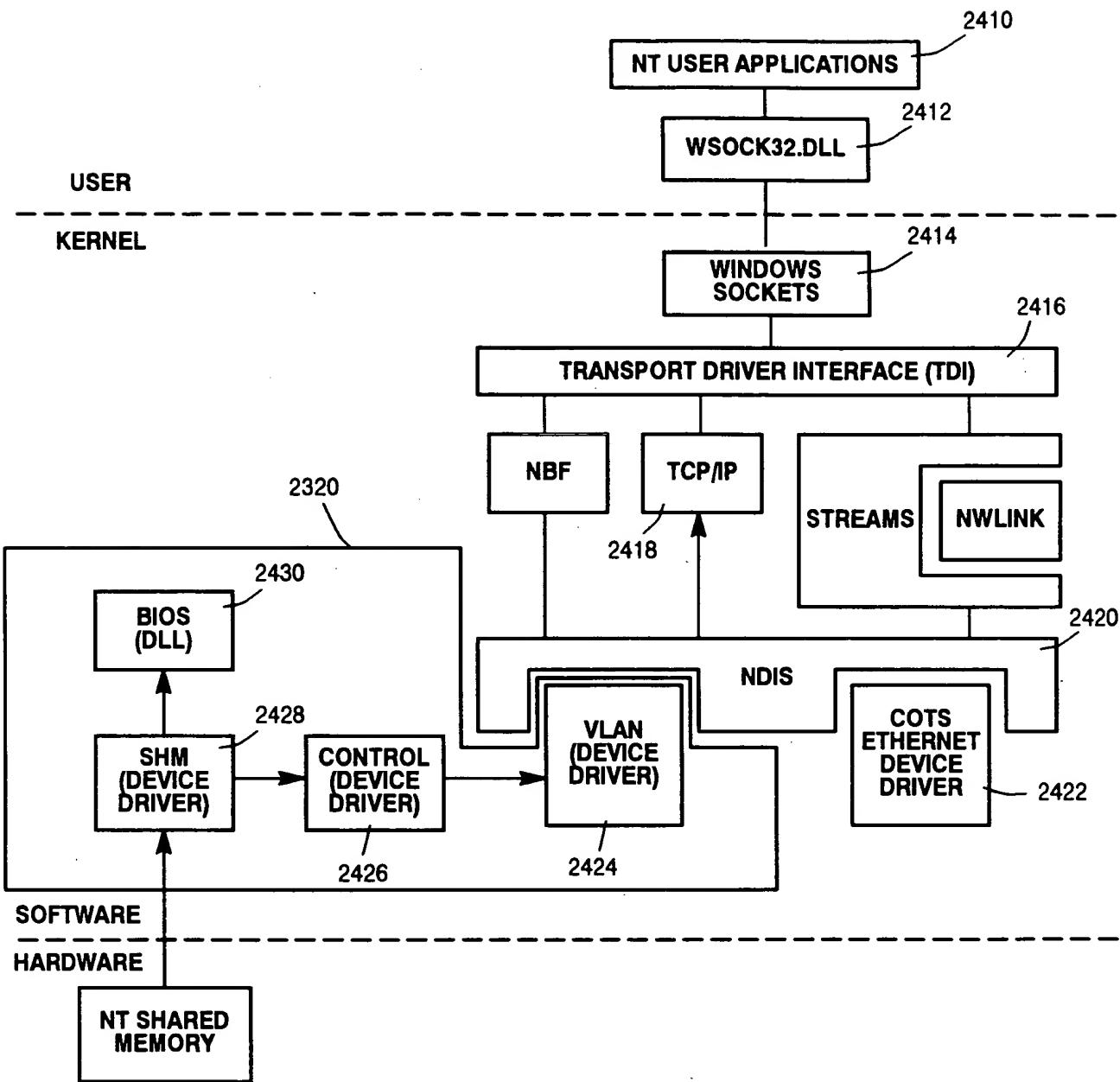


Figure 24

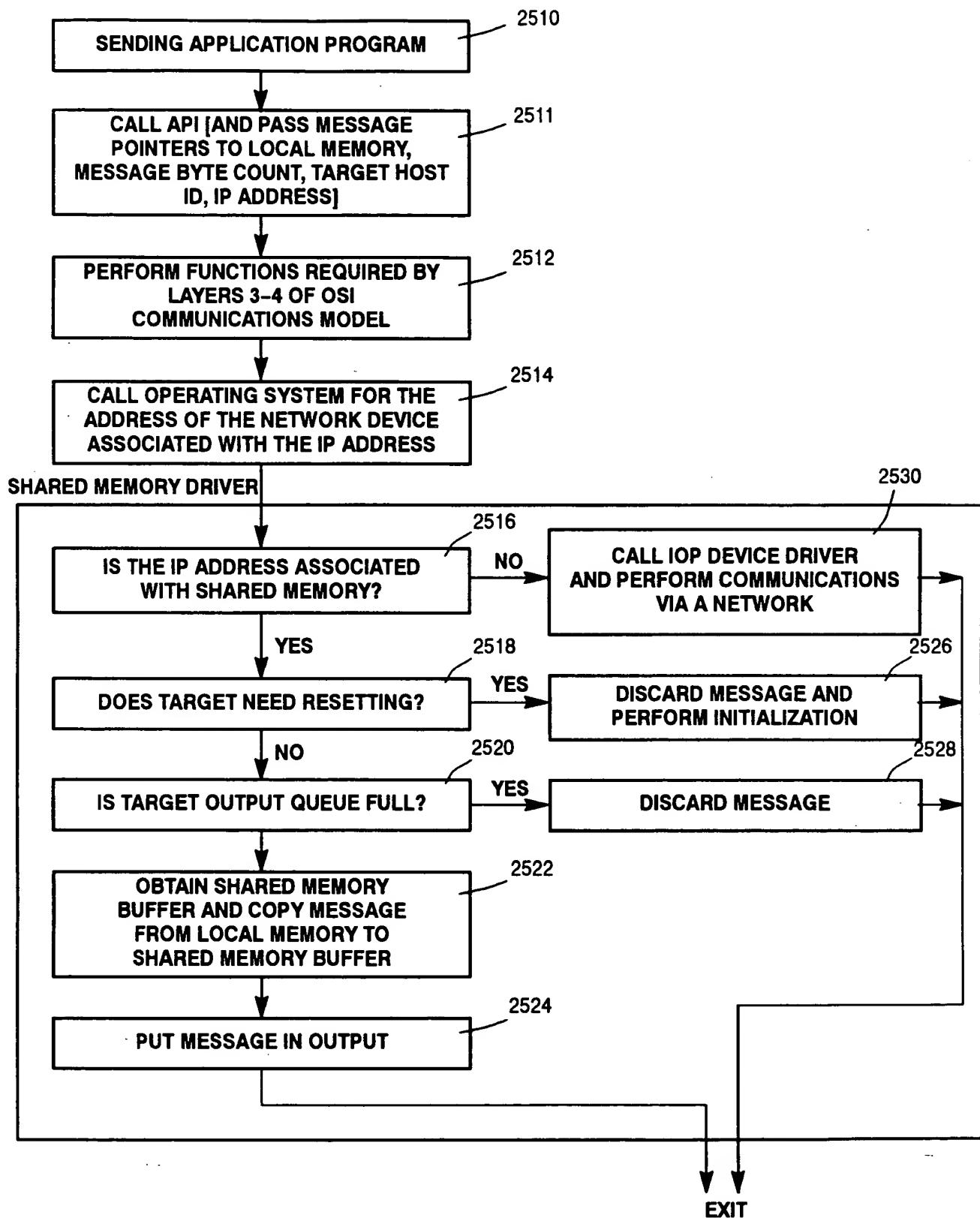


Figure 25

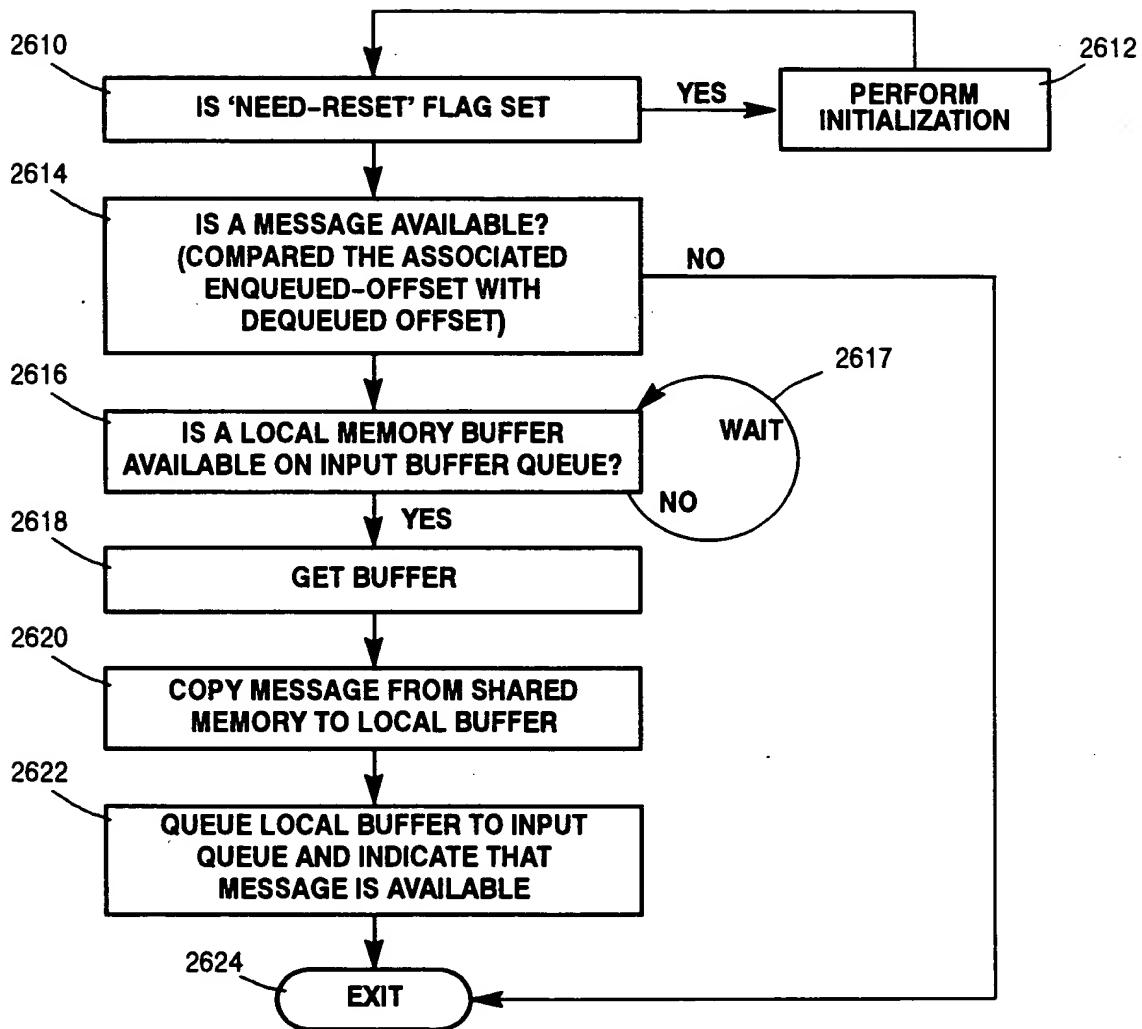


Figure 26

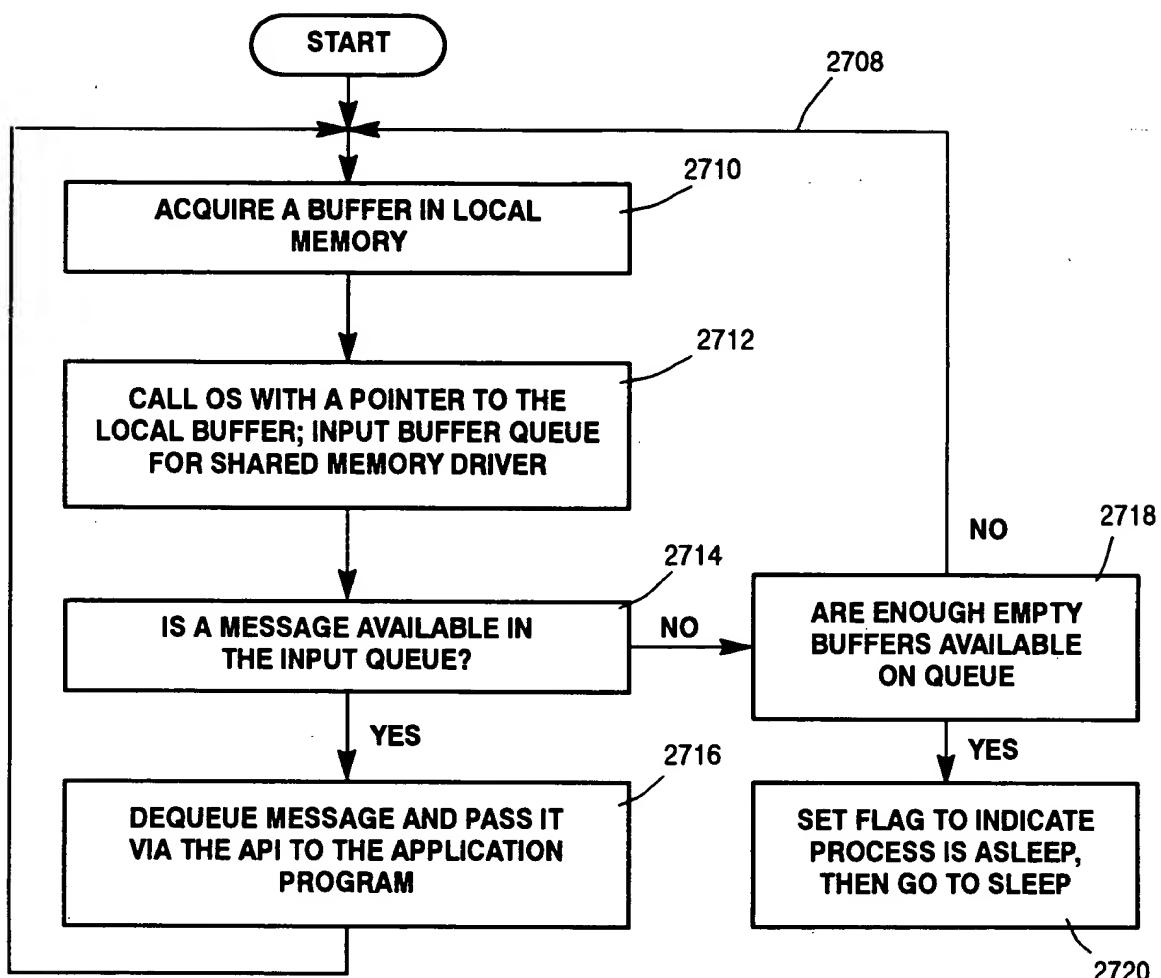


Figure 27

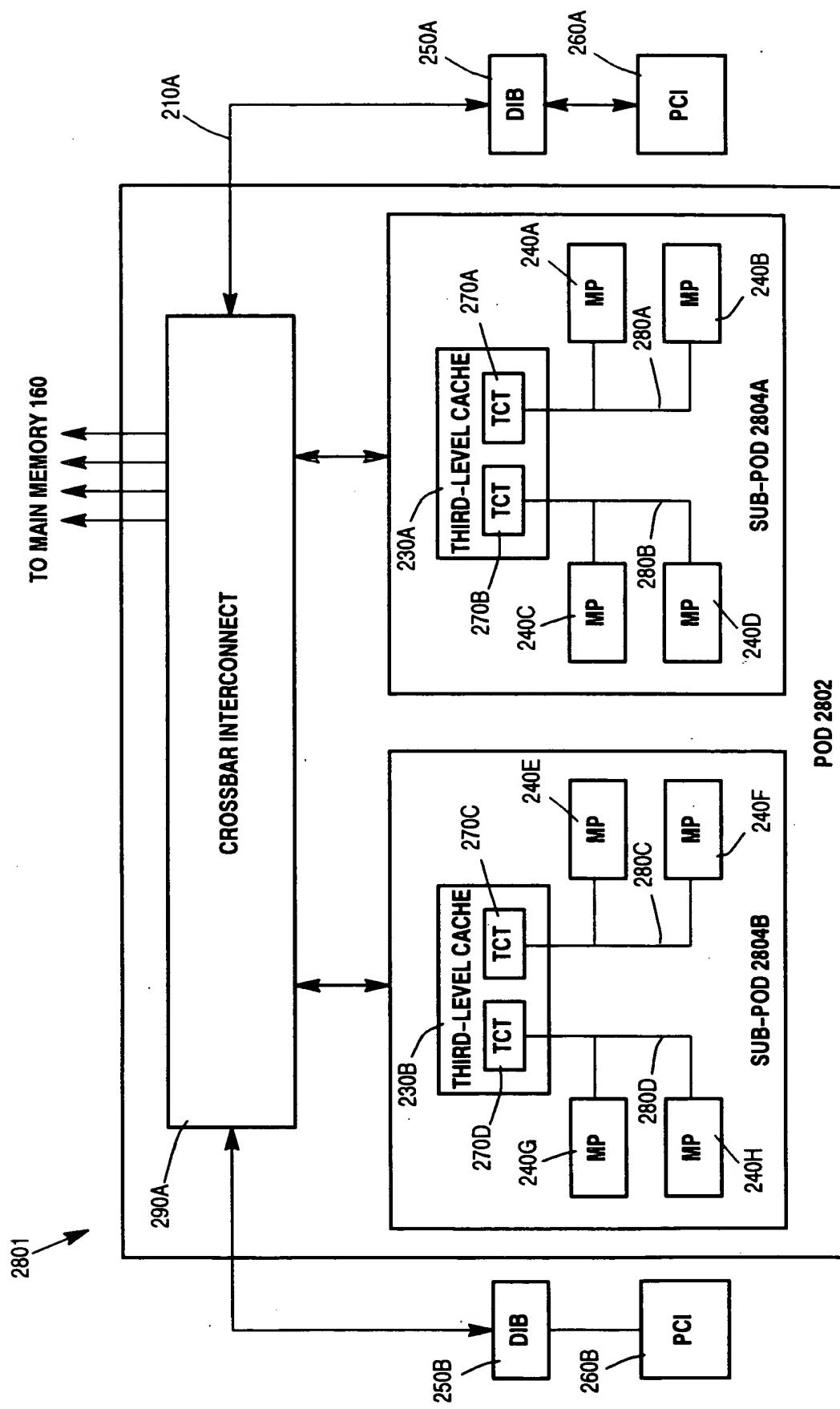


Figure 28

INPUT QUEUE HEADER CONTENTS
INPUT QUEUES POINTER
NUMBER OF INPUT QUEUES
INPUT QUEUE LENGTH
INPUT QUEUE SIGNAL SIZE
MAX NUMBER OF SIGNALS IN INPUT QUEUE

Figure 29

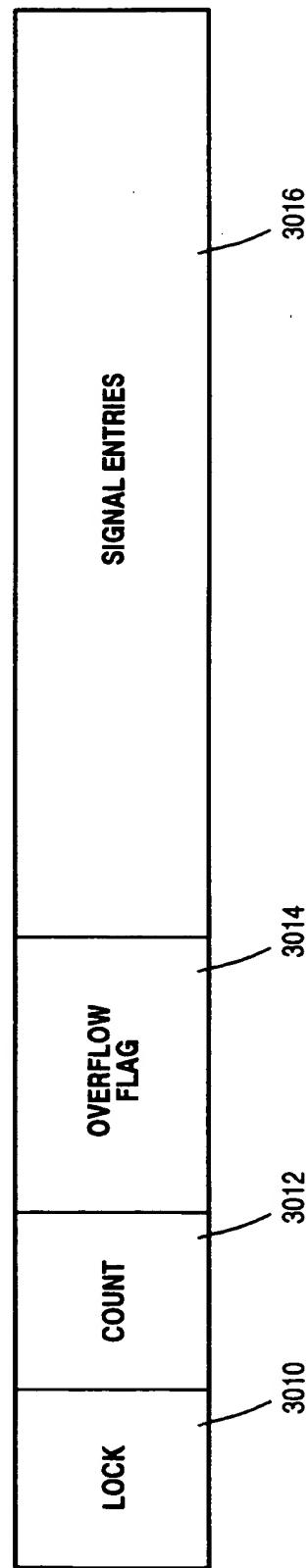


Figure 30

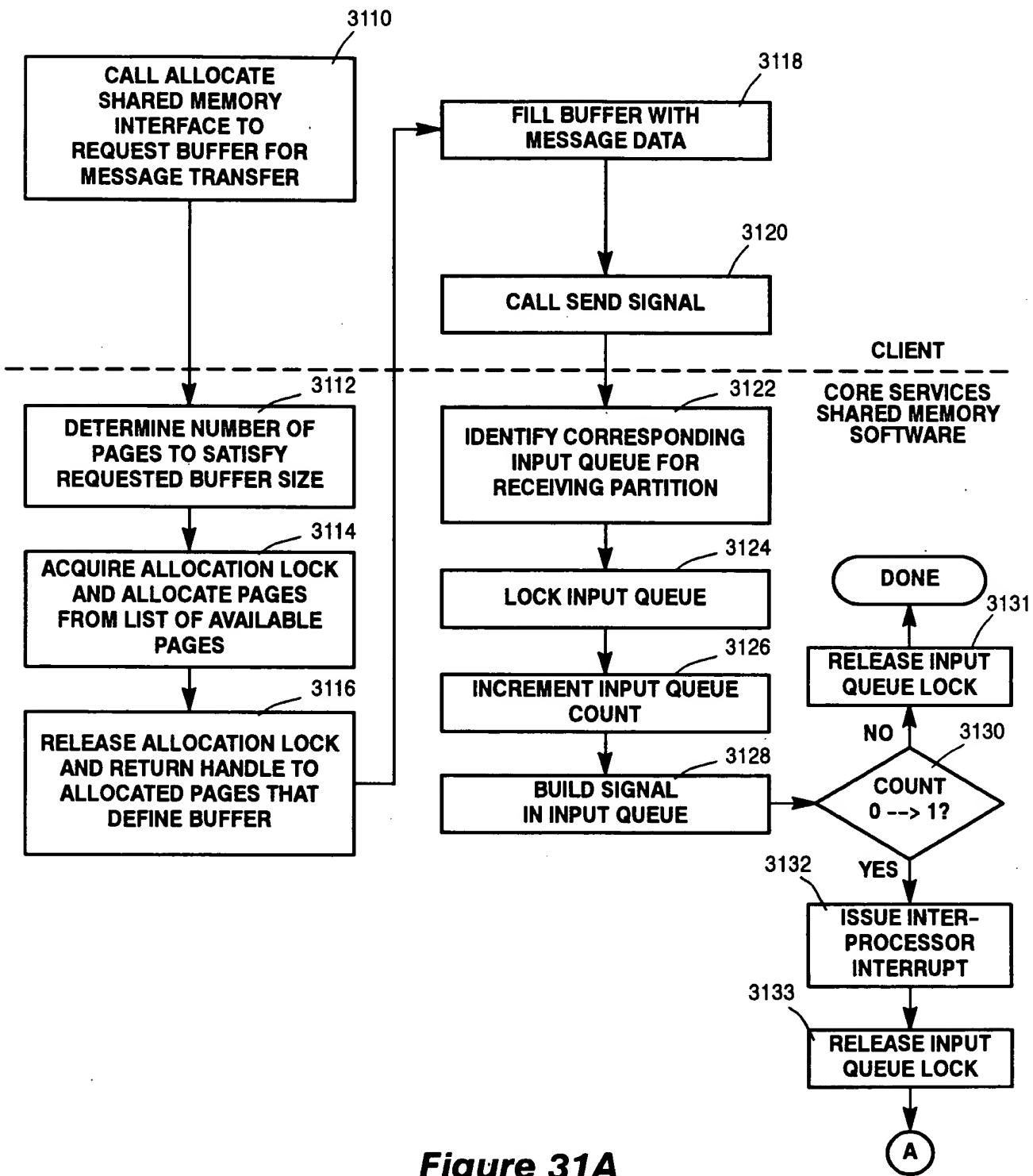


Figure 31A

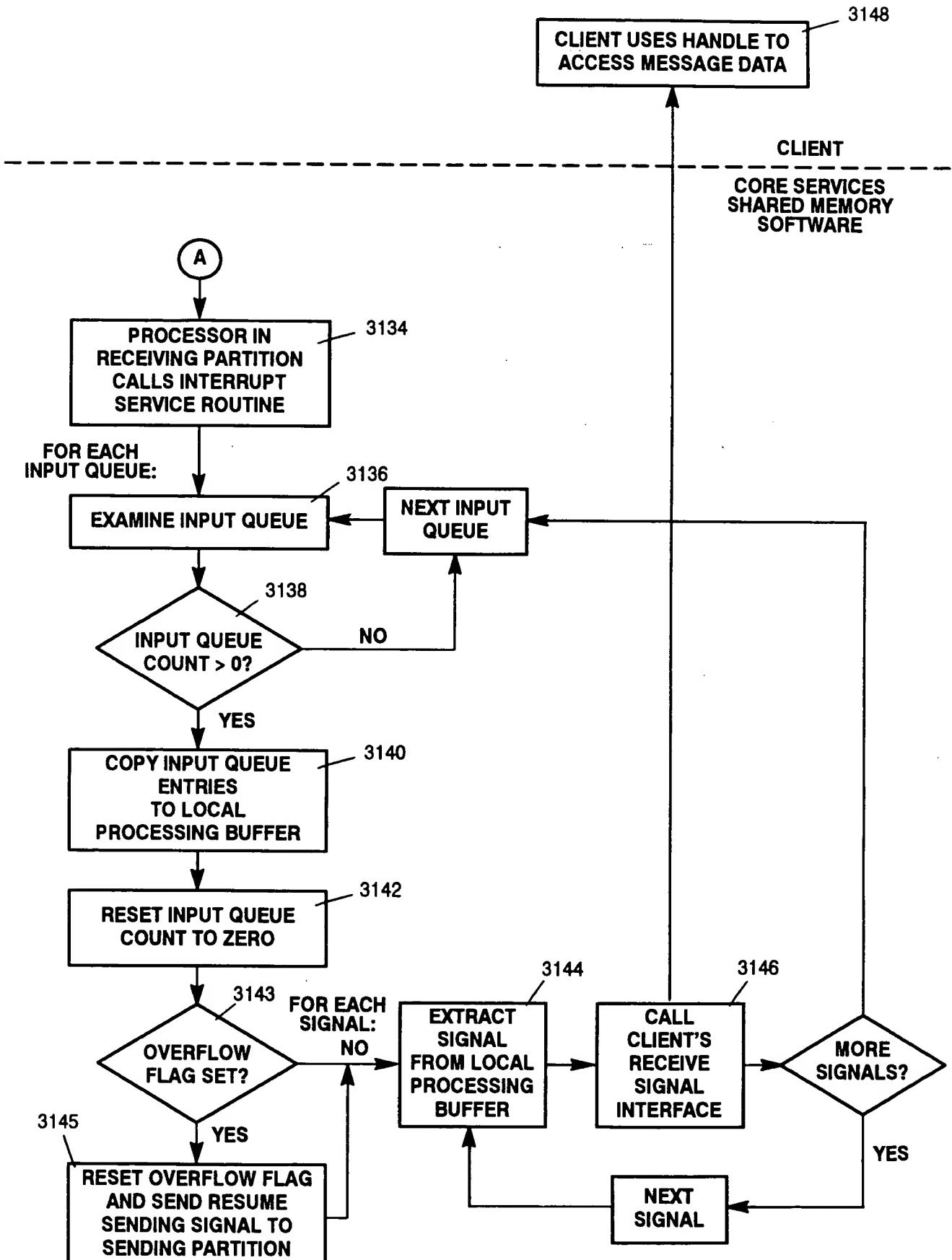


Figure 31B

PARTITION OWNERSHIP MASK	CLIENT GROUP ID	DEALLOCATION LOCK
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Figure 32A

PARTITION OWNERSHIP MASK	CLIENT GROUP ID	DEALLOCATION LOCK	TYPE 3 PAGE COUNT	TYPE 3 PAGE REFERENCE
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Figure 32B

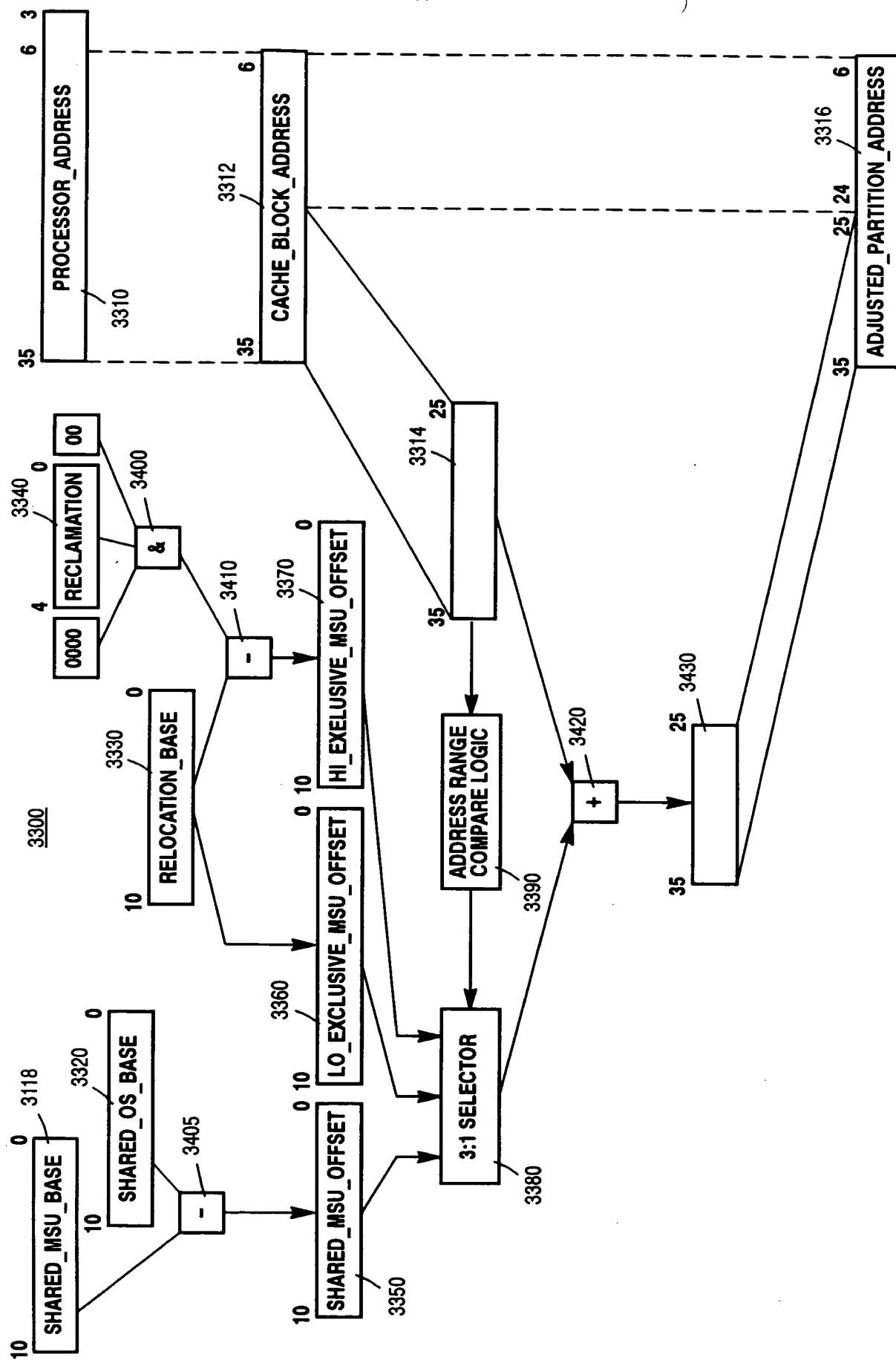


Figure 33

PARTITION 1

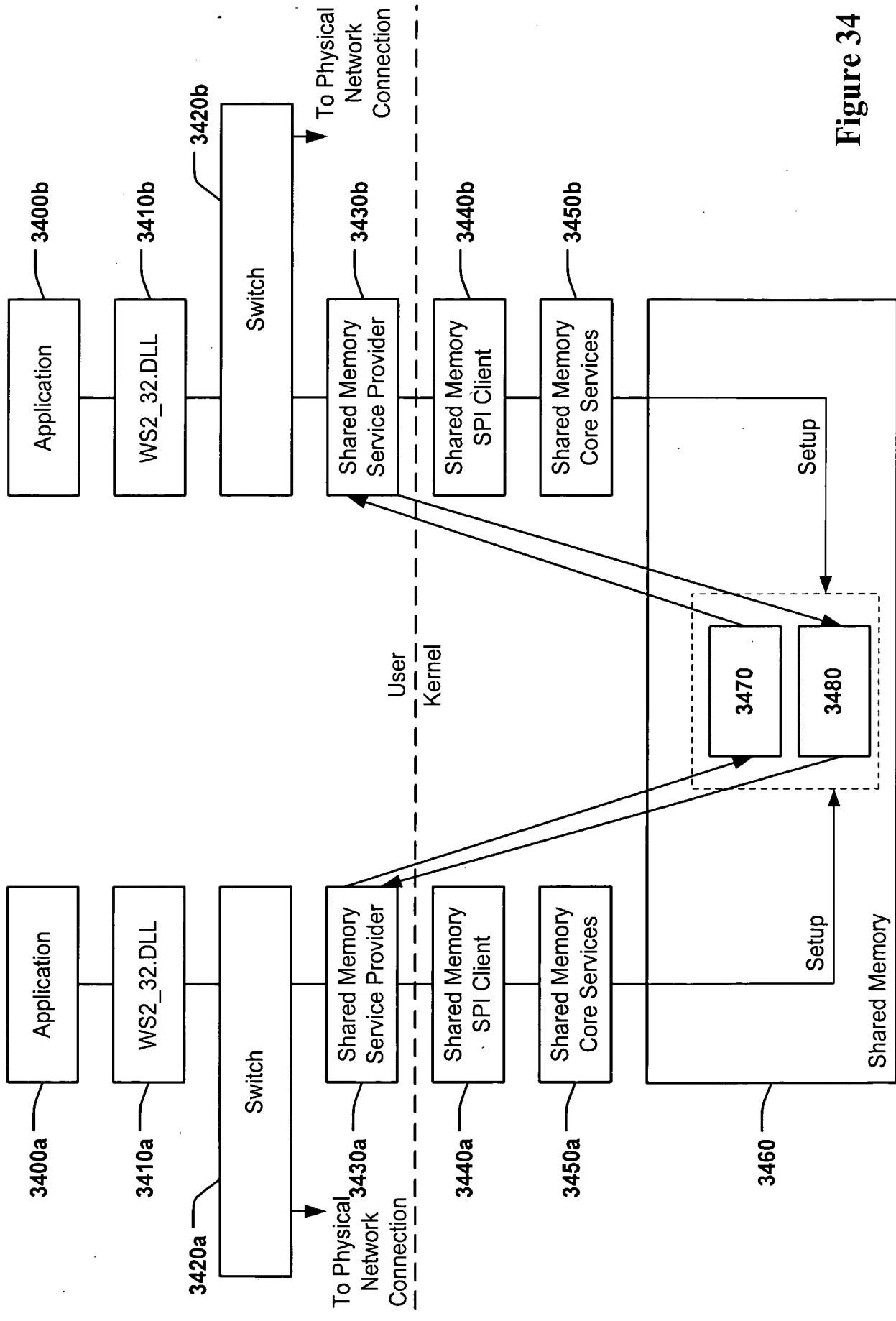


Figure 34

PARTITION 2

USYS-0094 "System and Method for Emulating Network Communications between Partitions of a Computer System"
S.B.Samuels-Woodcock Washburn et al.
215-568-3100

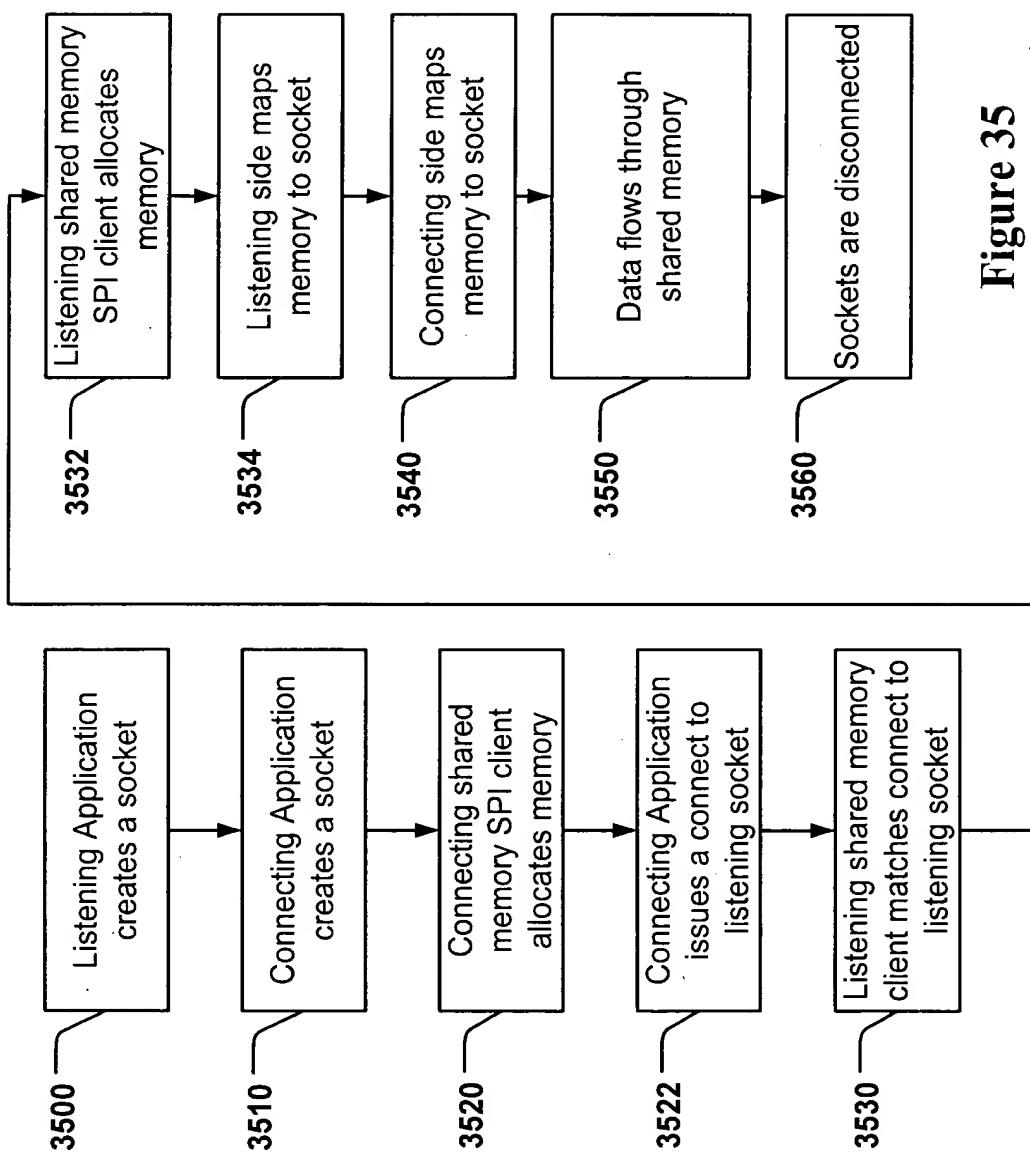


Figure 35

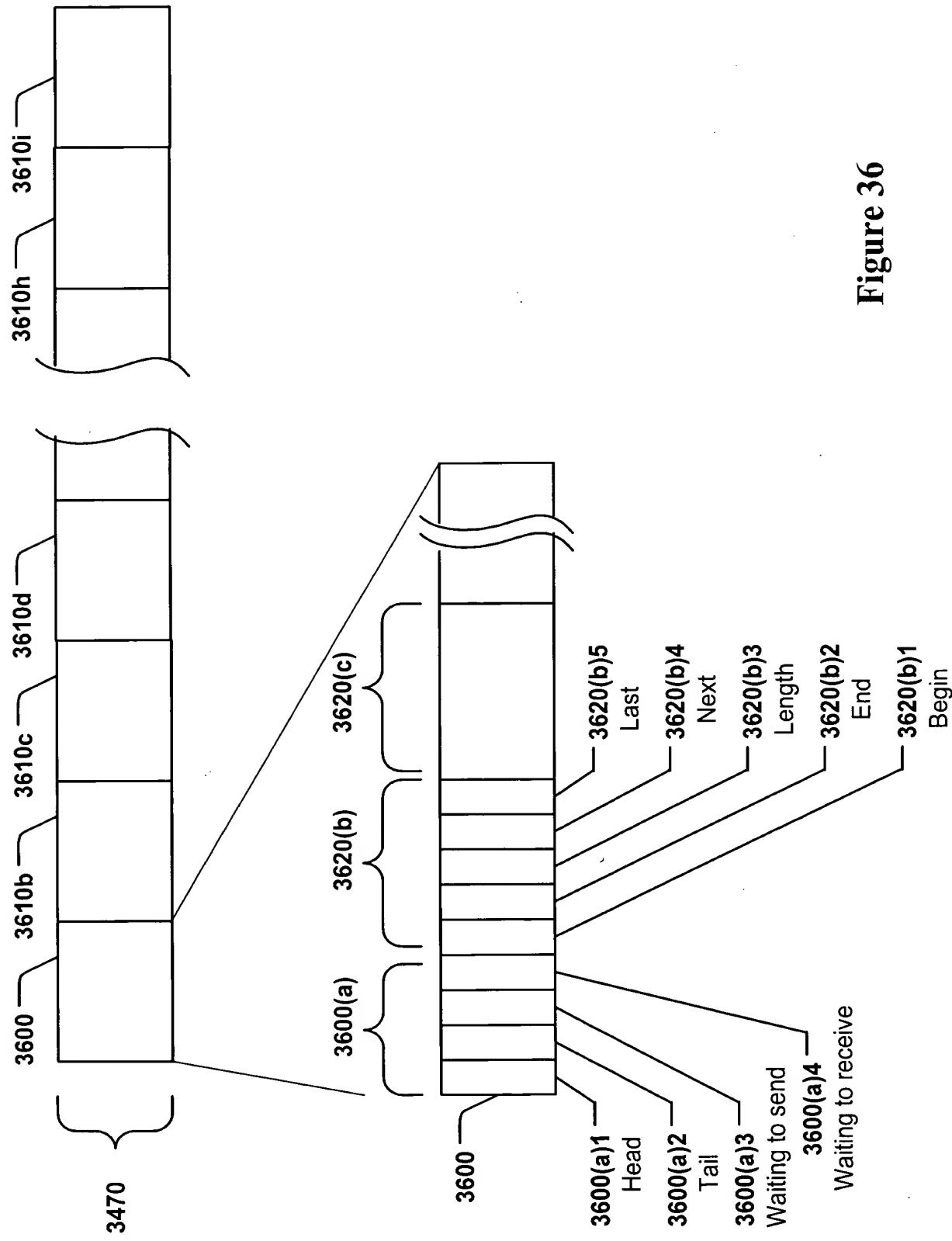


Figure 36

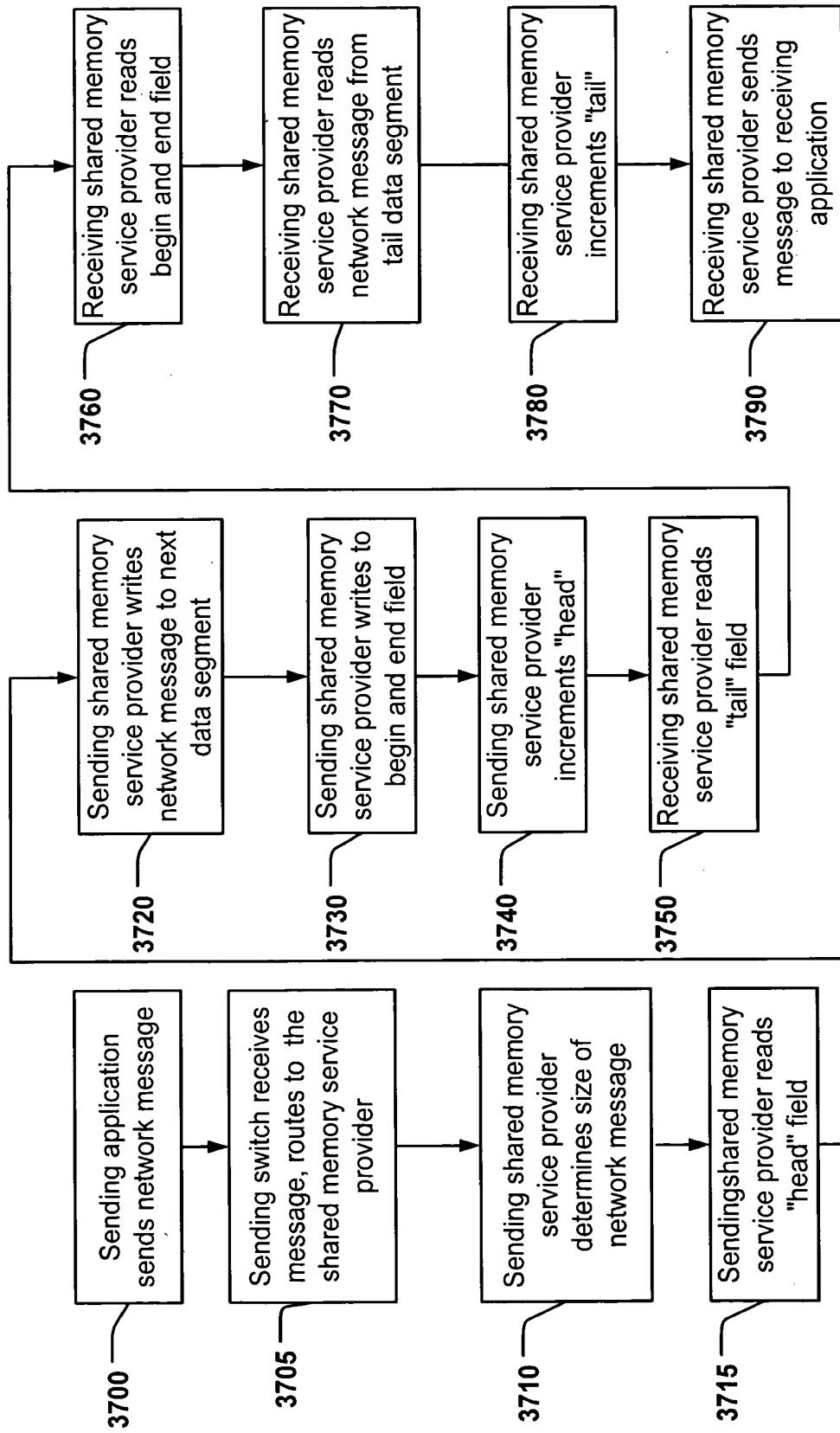


Figure 37